



QorIQ Communications Processors Built on Layerscape Architecture

# QorIQ LS1021A Communications Processor

Dual-core solution with integrated LCD controller for fanless applications

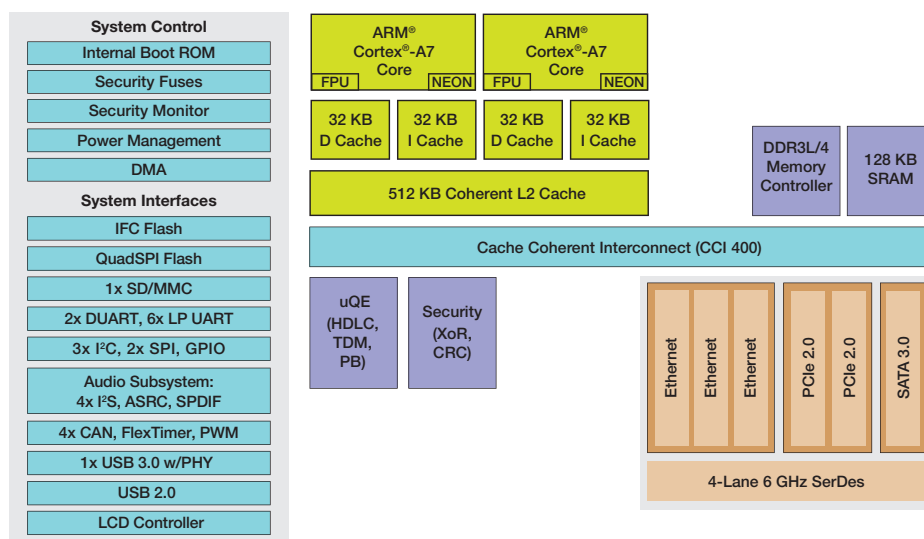
## Target Applications

- Enterprise AP routers for 802.11ac/n
- Multi-protocol IoT gateways
- Industrial and factory automation
- Mobile wireless routers
- Printing
- Building automation
- Smart energy

## Overview

The QorIQ LS1021A processor delivers extensive integration and power efficiency for fanless, small form factor networked applications. Incorporating dual ARM® Cortex®-A7 cores with ECC protection running up to 1.0 GHz, the QorIQ LS1021A processor delivers pre-silicon CoreMark® performance of over 6,000, as well as virtualization support, advanced security features and the broadest array of high-speed interconnects and optimized peripheral features ever offered in a sub-3 W processor.

## QorIQ LS1021A Processor Block Diagram



- Core Complex
- Basic Peripherals and Interconnect
- Accelerators and Memory Control
- Networking Elements



## Unparalleled Integration

The QorIQ LS1 family of devices was designed specifically to enable a new class of power-constrained applications by bringing together highly efficient ARM® cores and over twenty years of Freescale networking expertise and IP to offer the highest level of integration under 3 W. With ECC protection on both L1 and L2 caches, QUICC Engine support, USB 3.0, and a broad range of other peripheral and I/O features, the LS1 family of devices is purpose-built for multicore platforms that must perform more securely, intelligently and efficiently without sacrificing performance.

## Core Complex

The QorIQ LS1021A processor integrates dual ARM Cortex-A7 cores running up to 1.0 GHz with ECC protected L1 and L2 caches. Both cores feature 32 KB of L1 instruction and data cache, share up to 512 KB of coherent L2 cache, and feature the NEON SimD module and dual precision floating-point unit (FPU). The DDR memory controller supports 8-, 16- or 32-bit type 3L and four memory devices at up to 1600 MHz.

## QorIQ LS1021A Processor Features

<b>Dual ARM Cortex-A7 cores</b>	Extreme power efficiency, delivering pre-silicon performance at over 6,000 CoreMarks®. Typical total system power of 3 W for improved performance without increased power utilization.
<b>ECC-protected L1 and L2 cache memories</b>	The QorIQ LS1 family devices are the only processors in their class with ECC-protected caches and coherent 512 KB L2, adding performance and meeting networking requirements for high reliability.
<b>Rich connectivity and peripheral features, including PCI Express Gen 2, USB 3.0, SATA 3, IFC, QuadSPI, CAN</b>	High versatility that enables support for 802.11ac modules and high bandwidth connectivity for ASICs, 4G/LTE, SATA and low-cost NAND/NOR Flash
<b>LCD controller (DCU5)</b>	Touchscreen support adds integrated HMI featured for enhanced ease of use and BOM savings. Similar IP as Freescale Vybrid controller solutions and i.MX applications processors to allow for simple software migration.
<b>QUICC Engine</b>	Proven support required for industrial, building and factory protocols such as PROFIBUS, HDLC and TDM
<b>Support for virtualized Gb Ethernet</b>	Enables partitioning of CPU resources on low-power parts for increased system productivity
<b>DDR3L/4</b>	First in its class to offer support for DDR4 memory, ensuring continued performance efficiency

## System Interfaces and Networking

A four-lane, 6 GHz multi-protocol SerDes provides support for high-speed interfaces, including up to three Gigabit Ethernet ports with IEEE® 1588 support, dual DMA controlled PCI Express® generation 2.0 ports and a single SATA 3.0 port. The LS1021A processor also features dual USB controllers—one supporting SuperSpeed USB 3.0 with integrated PHY, the other supporting USB 2.0 functions. Additional interfaces include QuadSPI, IFC and support for SD/MMC. For network audio applications, the LS1021A processor includes support for both ASRC and SPDF. For industrial applications, the processor provides four CAN ports and up to 10 UARTS to support industrial protocols. In addition, Serial IO includes three I²C and two SPI interfaces.

## Scalability

A key advantage of QorIQ processors built on Layerscape architecture is the complete compatibility of features, including virtualization and cache coherency, as well as ISA between various QorIQ LS1 devices. This, together with pin and software compatibility between the other LS1 devices—the LS1020A and LS1022A processors—enables customers to simply and smoothly migrate applications between next-generation QorIQ families.

## Complete Enablement, Rich Ecosystem

For customer evaluation, the QorIQ LS1021A processor will be supported by the modular Freescale Tower System, which will feature an integrated on-board probe for further cost savings, along with third-party platforms developed by Freescale's embedded board solution partners. Evaluation kits include a Linux® 3.12 BSP with optimized drivers to support peripherals and a six-month evaluation license for CodeWarrior development tools.

All QorIQ LS series devices are supported by our extensive third-party ecosystem, the largest and most established in the communications market. In conjunction with our expertise and worldwide support infrastructure, the ecosystem helps customers accelerate their migration from both competitive solutions and from legacy Freescale devices, preserve investment costs and reduce time to market.

For more information, visit [freescale.com/QorIQ](http://freescale.com/QorIQ)

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