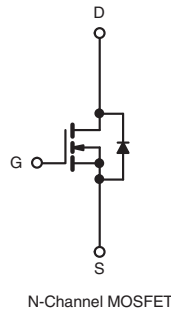
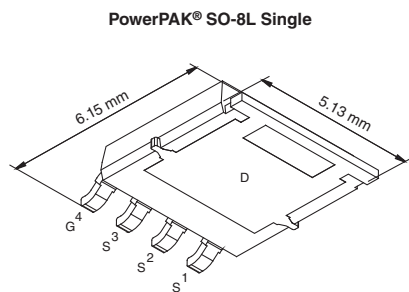


Automotive N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	60
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.023
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	0.032
I_D (A)	24
Configuration	Single



FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC
- AEC-Q101 Qualified^d
- Find out more about Vishay's Automotive Grade Product Requirements at: www.vishay.com/applications



ORDERING INFORMATION	
Package	PowerPAK SO-8L
Lead (Pb)-free and Halogen-free	SQJ850EP-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^a	I_D	$T_C = 25$ °C	24
		$T_C = 125$ °C	17
Continuous Source Current (Diode Conduction) ^a	I_S	24	A
Pulsed Drain Current ^b	I_{DM}	96	
Single Pulse Avalanche Current	I_{AS}	15	
Single Pulse Avalanche Energy	E_{AS}	11	mJ
Maximum Power Dissipation ^b	P_D	$T_C = 25$ °C	45
		$T_C = 125$ °C	15
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 175	°C
Soldering Recommendations (Peak Temperature) ^{e, f}		260	

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R_{thJA}	70	°C/W
Junction-to-Case (Drain)	R_{thJC}	3.3	

Notes

- Package limited.
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.
- See solder profile (www.vishay.com/ppg?73257). The PowerPAK SO-8L. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

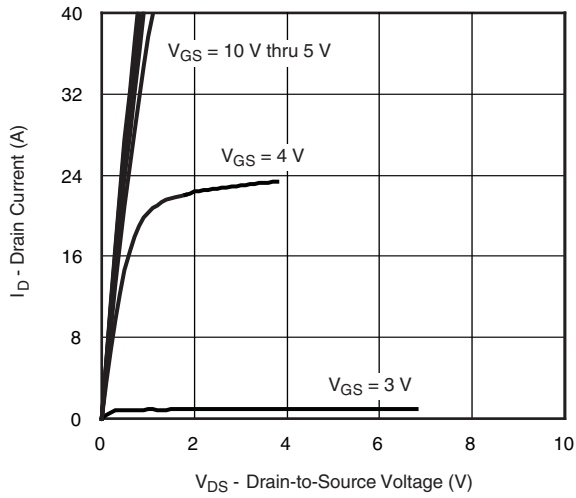
SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	60	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.5	2.0	2.5	
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 60 V	-	-	1	μA
		V _{GS} = 0 V, V _{DS} = 60 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V, V _{DS} = 60 V, T _J = 175 °C	-	-	150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V, V _{DS} ≥ 5 V	30	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 10.3 A	-	0.019	0.023	Ω
		V _{GS} = 4.5 V, I _D = 8.7 A	-	0.026	0.032	
		V _{GS} = 10 V, I _D = 10.3 A, T _J = 125 °C	-	0.034	0.040	
		V _{GS} = 10 V, I _D = 10.3 A, T _J = 175 °C	-	0.042	0.051	
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 10.3 A	-	29	-	S
Dynamic^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 30 V, f = 1 MHz	-	980	1225	pF
Output Capacitance	C _{oss}		-	170	215	
Reverse Transfer Capacitance	C _{rss}		-	70	88	
Total Gate Charge ^c	Q _g	V _{GS} = 10 V, V _{DS} = 30 V, I _D = 6 A	-	20	30	nC
Gate-Source Charge ^c	Q _{gs}		-	2.9	-	
Gate-Drain Charge ^c	Q _{gd}		-	4.4	-	
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 30 V, R _L = 30 Ω I _D ≅ 1 A, V _{GEN} = 10 V, R _g = 6.0 Ω	-	21	29	ns
Rise Time ^c	t _r		-	15	22	
Turn-Off Delay Time ^c	t _{d(off)}		-	22	35	
Fall Time ^c	t _f		-	8	12	
Source-Drain Diode Ratings and Characteristics^b						
Pulsed Current ^a	I _{SM}		-	-	96	A
Forward Voltage	V _{SD}	I _F = 3.8 A, V _{GS} = 0 V	-	0.8	1.2	V

Notes

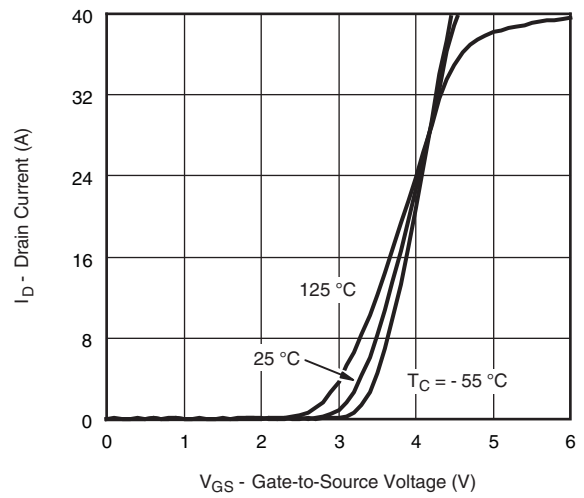
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

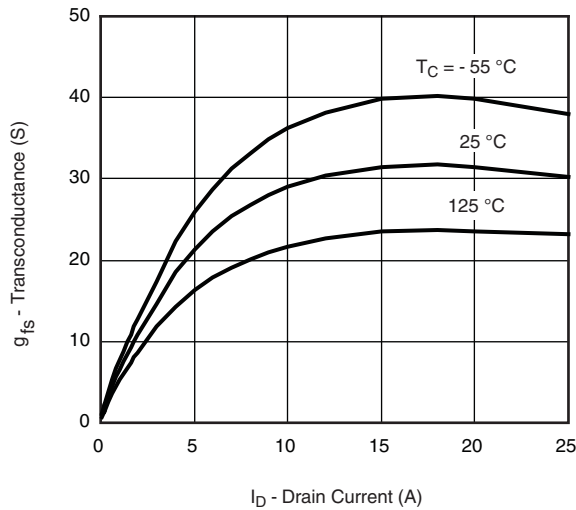
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



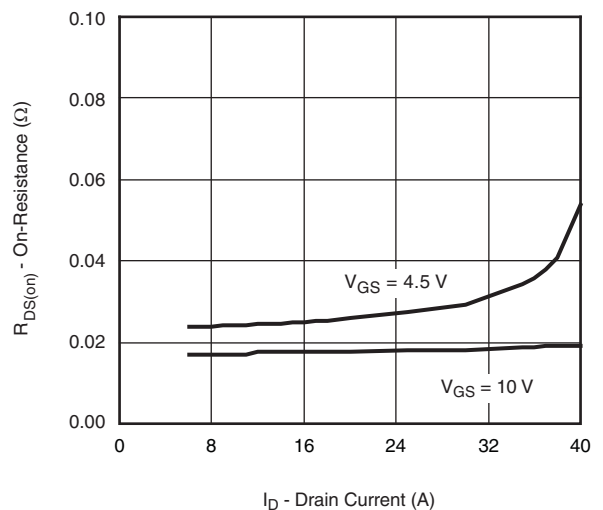
Output Characteristics



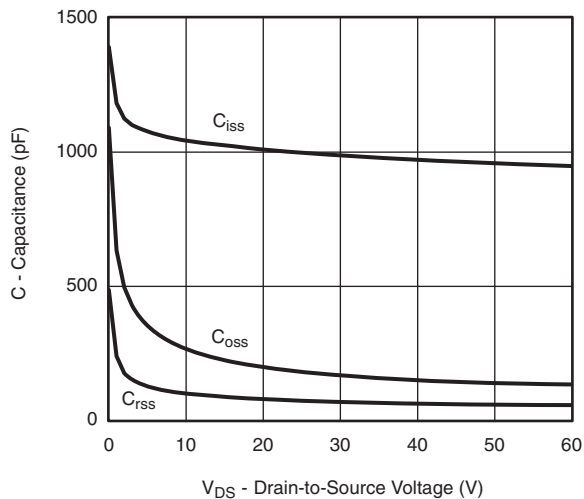
Transfer Characteristics



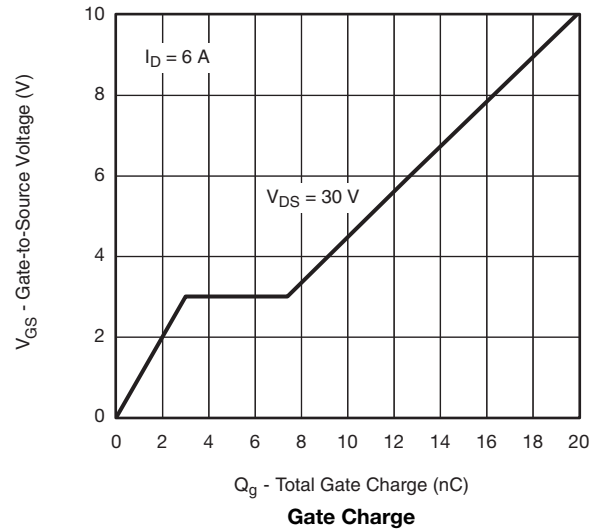
Transconductance



On-Resistance vs. Drain Current

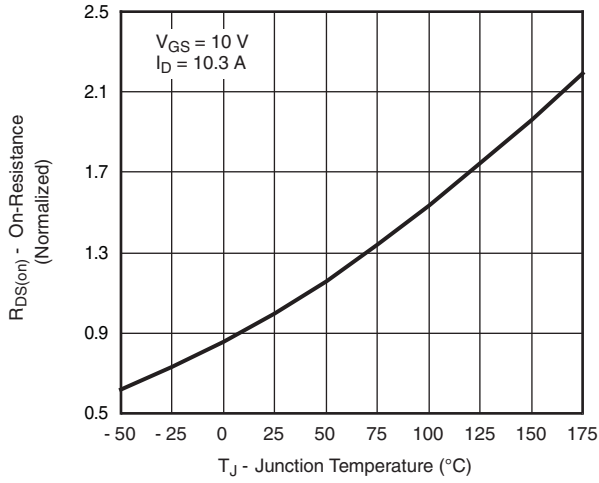


Capacitance

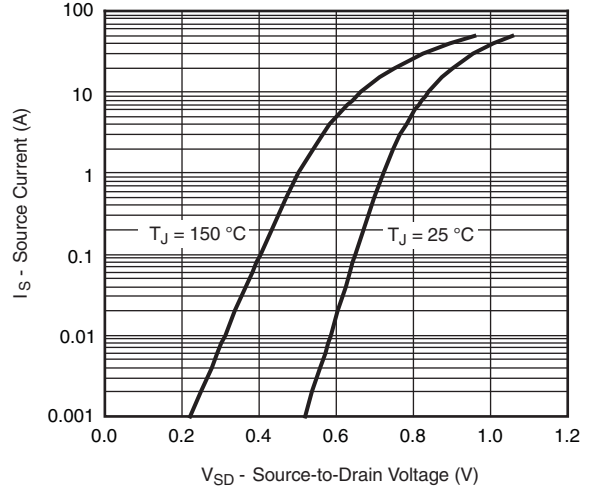


Gate Charge

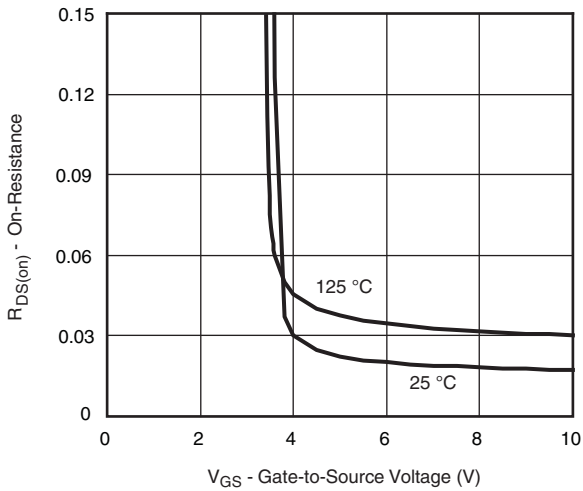
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



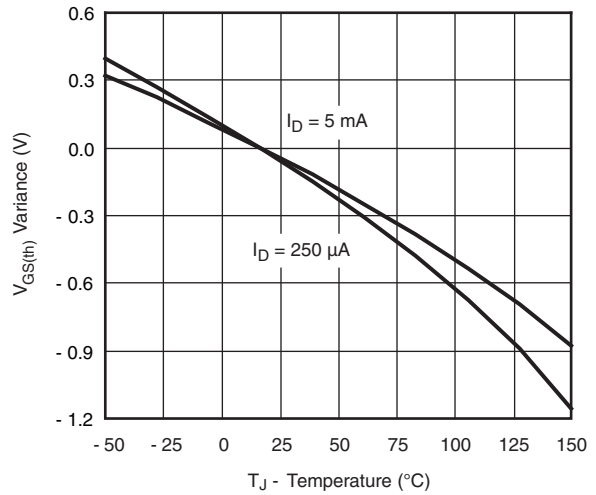
On-Resistance vs. Junction Temperature



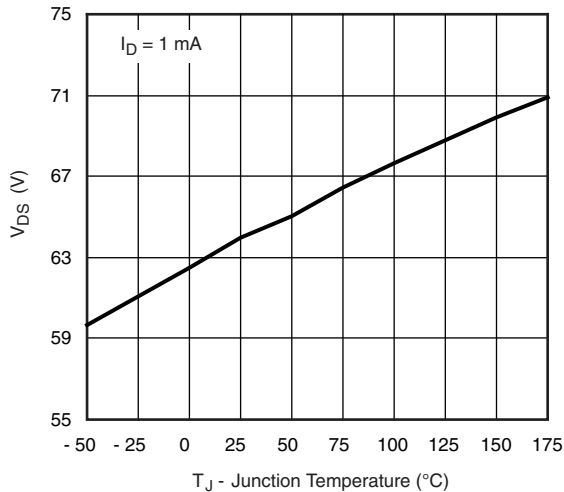
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

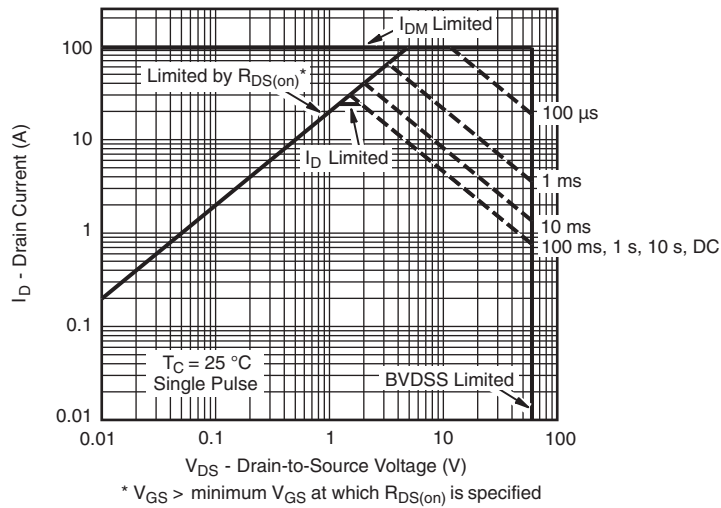


Threshold Voltage

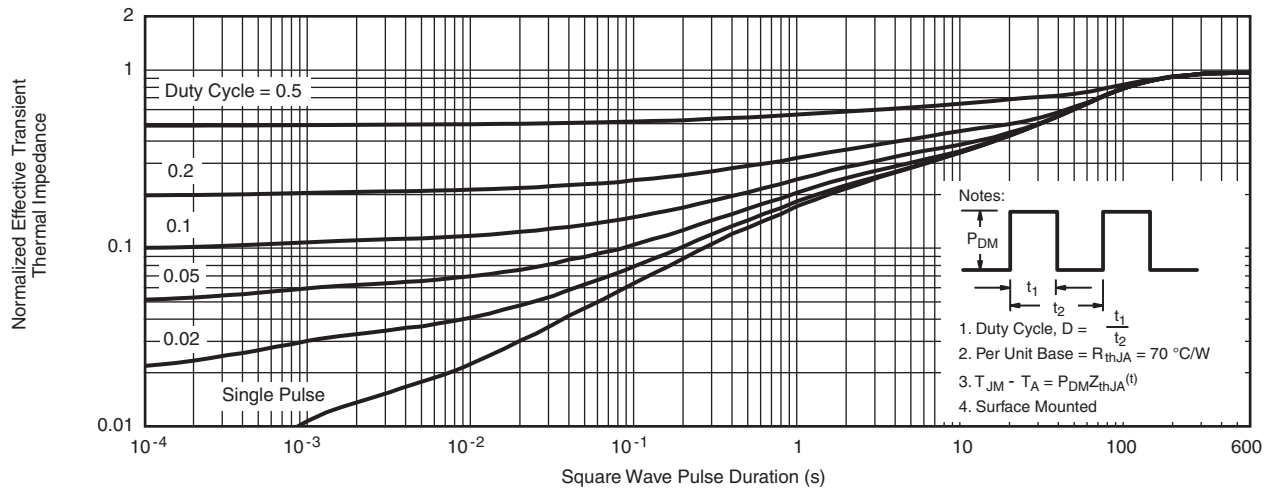


Drain Source Breakdown vs. Junction Temperature

THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)

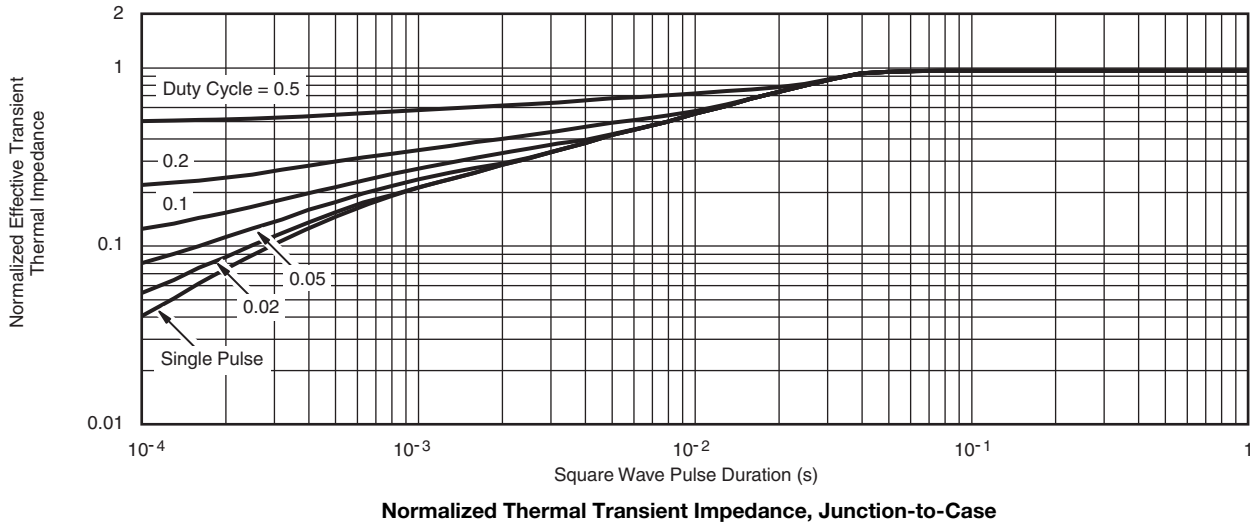


Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^\circ\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Case ($25\text{ }^\circ\text{C}$)
 are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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