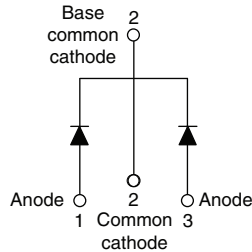


High Performance Schottky Generation 5.0, 2 x 8 A


TO-220AB

FEATURES

- 175 °C high performance Schottky diode
- Very low forward voltage drop
- Extremely low reverse leakage
- Optimized V_F vs. I_F trade off for high efficiency
- Increased ruggedness for reverse avalanche capability
- RBSOA available
- Negligible switching losses
- Submicron trench technology
- Full lead (Pb)-free and RoHS compliant devices
- Designed and qualified for industrial level


**RoHS
COMPLIANT**
PRODUCT SUMMARY

| | |
|------------------------|---------|
| $I_{F(AV)}$ | 2 x 8 A |
| V_R | 100 V |
| V_F at 8 A at 125 °C | 0.58 V |

APPLICATIONS

- High efficiency SMPS
- Automotive
- High frequency switching
- Output rectification
- Reverse battery protection
- Freewheeling
- Dc-to-dc systems
- Increased power density systems

MAJOR RATINGS AND CHARACTERISTICS

| SYMBOL | CHARACTERISTICS | VALUES | UNITS |
|-----------|--|-------------|-------|
| V_{RRM} | | 100 | V |
| V_F | 8 Apk, $T_J = 125$ °C (typical, per leg) | 0.55 | |
| T_J | Range | - 55 to 175 | °C |

VOLTAGE RATINGS

| PARAMETER | SYMBOL | TEST CONDITIONS | 16CTT100 | UNITS |
|----------------------------|--------|-----------------|----------|-------|
| Maximum DC reverse voltage | V_R | $T_J = 25$ °C | 100 | V |

ABSOLUTE MAXIMUM RATINGS

| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
|--|-------------|--|--------------------------------|-------|
| Maximum average forward current <small>per leg</small> <small>per device</small> | $I_{F(AV)}$ | 50 % duty cycle at $T_C = 163$ °C, rectangular waveform | 8 | A |
| | | | 16 | |
| Maximum peak one cycle non-repetitive surge current per leg | I_{FSM} | 5 μ s sine or 3 μ s rect. pulse | 850 | |
| | | 10 ms sine or 6 ms rect. pulse | 210 | |
| Non-repetitive avalanche energy per leg | E_{AS} | $T_J = 25$ °C, $I_{AS} = 1.5$ A, $L = 60$ mH | 67 | mJ |
| Repetitive avalanche current per leg | I_{AR} | Limited by frequency of operation and time pulse duration so that $T_J < T_{J \text{ max.}}$. I_{AS} at $T_J \text{ max.}$ as a function of time pulse See fig. 8 | I_{AS} at $T_J \text{ max.}$ | A |



| ELECTRICAL SPECIFICATIONS | | | | | | |
|---------------------------------|----------------|---|-----------------------------------|--------|------------------|---------------|
| PARAMETER | SYMBOL | TEST CONDITIONS | TYP. | MAX. | UNITS | |
| Forward voltage drop per leg | $V_{FM}^{(1)}$ | 8 A | $T_J = 25\text{ }^\circ\text{C}$ | - | 0.72 | V |
| | | 16 A | | - | 0.85 | |
| | | 8 A | $T_J = 125\text{ }^\circ\text{C}$ | - | 0.58 | |
| | | 16 A | | - | 0.69 | |
| Reverse leakage current per leg | $I_{RM}^{(1)}$ | $T_J = 25\text{ }^\circ\text{C}$ | $V_R = \text{Rated } V_R$ | - | 65 | μA |
| | | $T_J = 125\text{ }^\circ\text{C}$ | | - | 4 | mA |
| Junction capacitance per leg | C_T | $V_R = 5\text{ }V_{DC}$ (test signal range 100 kHz to 1 MHz) $25\text{ }^\circ\text{C}$ | 520 | - | pF | |
| Series inductance per leg | L_S | Measured lead to lead 5 mm from package body | 8.0 | - | nH | |
| Maximum voltage rate of change | dV/dt | Rated V_R | - | 10 000 | V/ μs | |

Note

(1) Pulse width < 300 μs , duty cycle < 2 %

| THERMAL - MECHANICAL SPECIFICATIONS | | | | |
|---|----------------|--------------------------------------|-------------|------------------------|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| Maximum junction and storage temperature range | T_J, T_{Stg} | | - 55 to 175 | $^\circ\text{C}$ |
| Maximum thermal resistance, junction to case per leg | R_{thJC} | DC operation | 2 | $^\circ\text{C/W}$ |
| Maximum thermal resistance, junction to case per device | | | 1 | |
| Typical thermal resistance, case to heatsink | R_{thCS} | Mounting surface, smooth and greased | 0.5 | |
| Approximate weight | | | 2 | g |
| | | | 0.07 | oz. |
| Mounting torque | minimum | | 6 (5) | kgf · cm (lbf · in) |
| | maximum | | 12 (10) | |
| Marking device | | Case style TO-220AB (JEDEC) | 16CTT100 | |

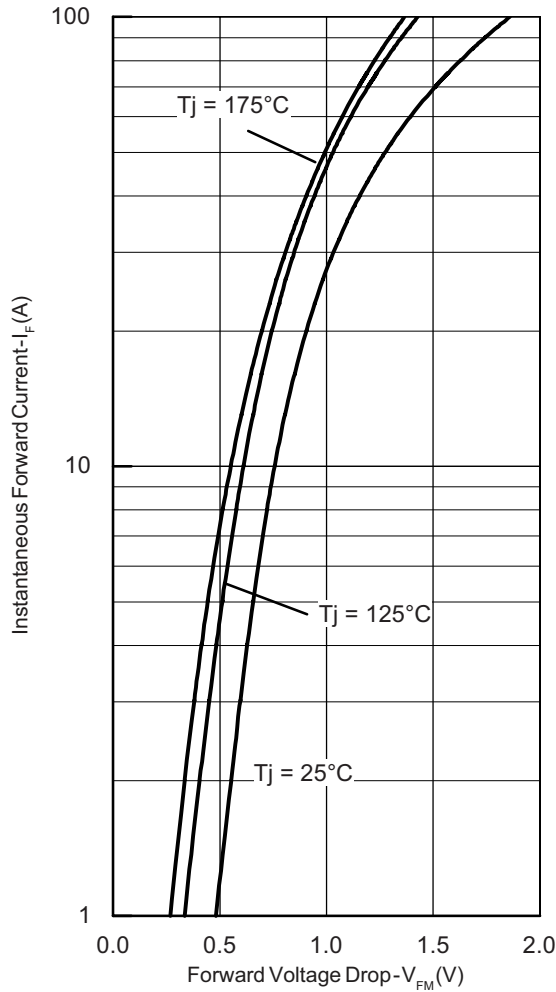


Fig. 1 - Maximum Forward Voltage Drop Characteristics

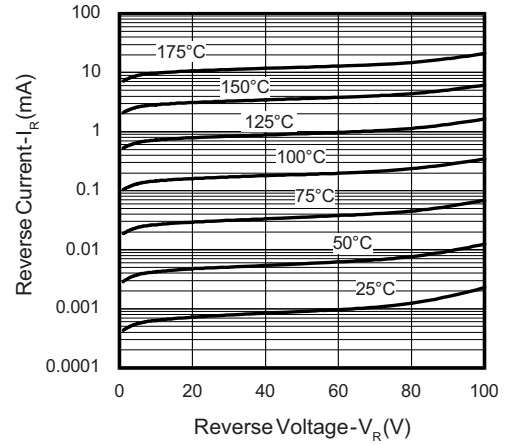


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

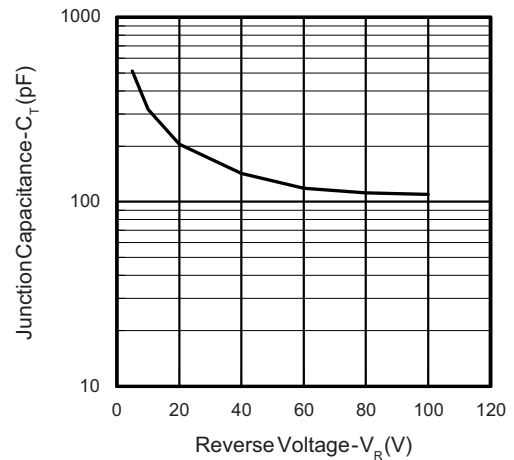


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

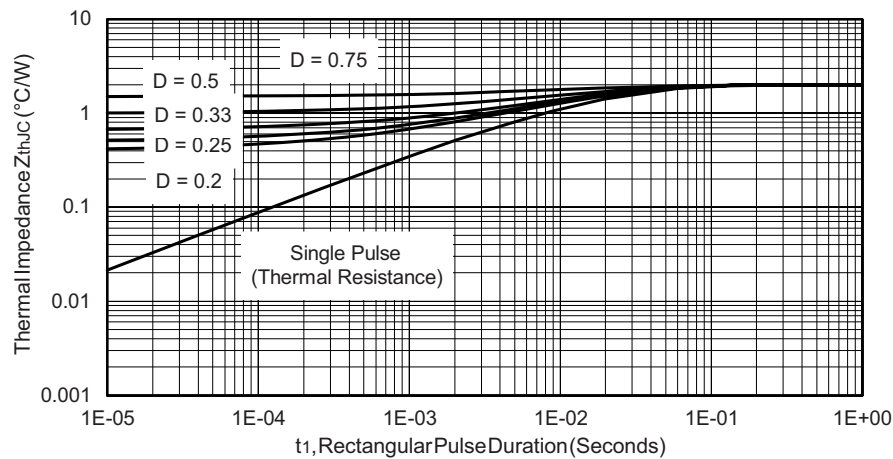


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

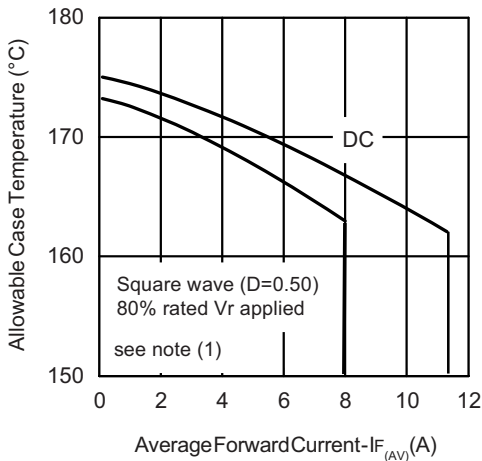


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

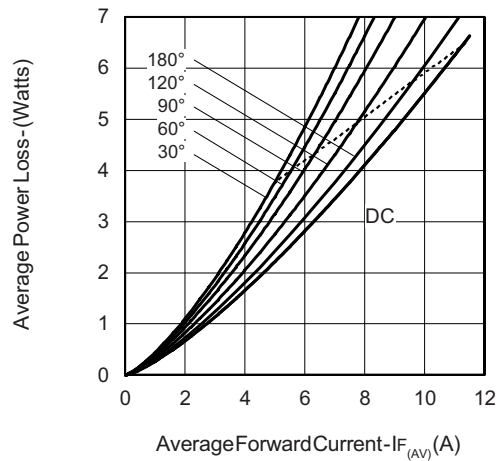


Fig. 6 - Forward Power Loss Characteristics

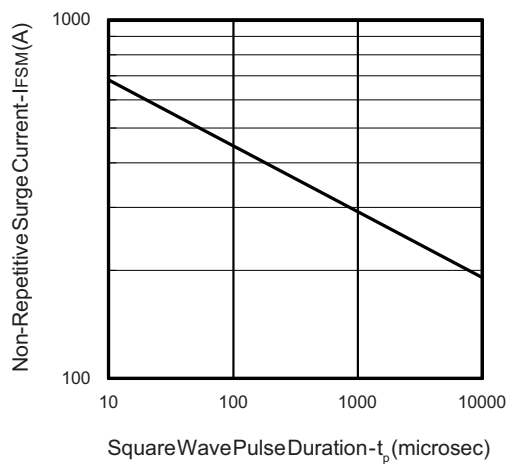


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$
 $P_d = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);}$
 $P_{d_{REV}} = \text{Inverse power loss} = V_{R1} \times I_R (1 - D); I_R \text{ at } V_{R1} = 80\% \text{ rated } V_R$



High Performance Schottky Generation 5.0, 2 x 8 A Vishay High Power Products

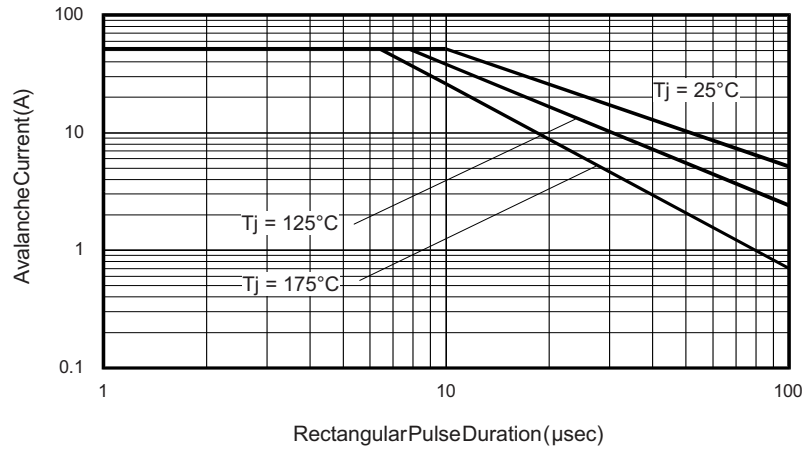


Fig. 8 - Reverse Bias Safe Operating Area (Avalanche Current vs. Rectangular Pulse Duration)

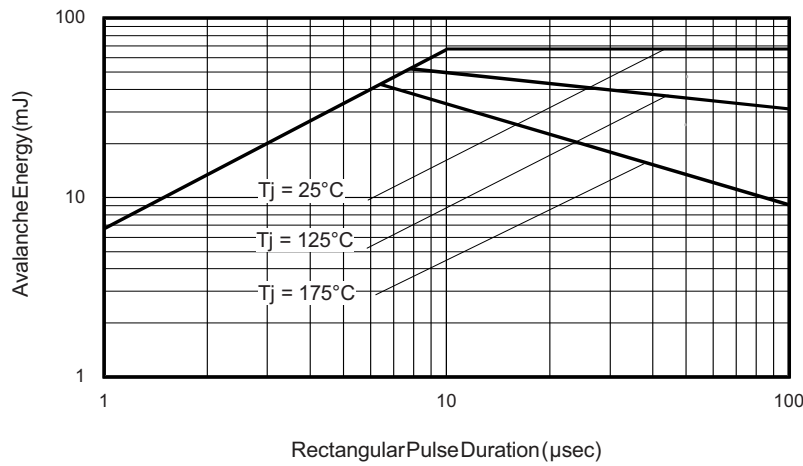
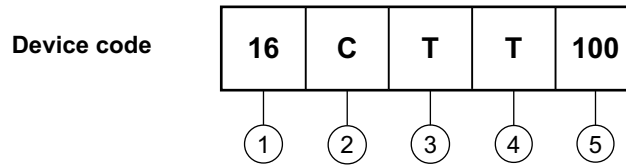


Fig. 9 - Reverse Bias Safe Operating Area (Avalanche Energy vs. Rectangular Pulse Duration)



ORDERING INFORMATION TABLE



- 1** - Current rating (16 A)
- 2** - Circuit configuration:
C = Common cathode
- 3** - Package:
T = TO-220
- 4** - T = Trench
- 5** - Voltage code (100 V)

Tube standard pack quantity: 50 pieces

| LINKS TO RELATED DOCUMENTS | |
|----------------------------|---|
| Dimensions | http://www.vishay.com/doc?95222 |
| Part marking information | http://www.vishay.com/doc?95225 |
| SPICE model | http://www.vishay.com/doc?95229 |



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