

SPECIFICATION

FOR LCD MODULE

MODEL NO:	TM161ABCWVBYA
CUSTOMER:	
CUSTOMER P/N.	
VERSION	V1.0
CUSTOMER	
APPROVED	

- □ Preliminary specification
- ■Final specification

PREPARED BY	CHECKED BY	CHECKED BY QA DEPT.			

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REVISION RECORD

Version	Page	Revision Items	Name	Date
1.0	1	First release for new IC	KXH	2008.01.11



TABLE OF CONTENTS

	Page
1. Description	3
2. Features	3
3. Absolute maximum ratings	4
4. Mechanical Characteristics	4
5. Circuit	6
6. Interface description	8
7. Instruction Code & Timing characteristics	9
8. Electrical Specifications	14
9. LED backlight characteristics	14
10. Optical Characteristics	15
11. Reliability	19
12. Quality level	21
13. Precautions for Use of LCD Modules	27
14 LCD Module Part Numbering System	20



1 Description

The TM161ABCWVBYA, Character LCM unit consists of 16-character x 1-line dot-matrix(5x8 dot) LCD panel, LCD driver, controller LSI on a single PCB. Incorporating mask ROM-based character generator and display data RAM in the controller LSI, the unit can efficiently display the desired character under microprocessor control.

- Wide viewing direction.
- Wide Operating temperature.
- Requirements on environmental protection: RoHS.

2 Features

Item	Contents
I CD tune	STN
LCD type	Positive
LCD Duty	1/16
LCD Bias	1/5
Polarizer	Transflective
LCD background color	Yellow-Green
Segment color	Blue-Black
Backlighting	LED
Backlighting type	Edge
Backlighting color	Yellow-Green
Backlighting drive	110mA
View direction	6:00 (wide view direction)
Operating temperature	-20℃ ~ +70℃
Storage temperature	-30℃ ~ +80℃
Controller	ST7066U
Frame	SPCC (Black)
Technology	СОВ
Power supply	VDD = 5.0V
Data Transfer	8 Bit parallel

Notes:

- Color tone can slightly change with temperature and driving voltage.
- Color tone will be changed by backlight.



3 Absolute maximum ratings

/Mithout I	ED backlighting	7 T2-25 ℃\
(VVIII)OUL L	.ED backiidhlind	າ, ≀a=25 ປັ

Parameter	Symbol	Min	Max	Unit	Remark
Logic circuit supply voltage	V_{CC}	-0.3	+7.0	V	
LCD driving voltage	V_{LCD}	VCC-10.0	VCC+0.3	V	
Operating temperature range	Тор	-20	+70	$^{\circ}$ C	No
Storage temperature range	Tst	-30	+80	$^{\circ}$ C	Condensation

Note:

- LCD operating voltage V_{LCD}=V_{CC} −V_{EE}
- If the module is above these absolute maximum ratings. It may become permanently damaged.
- V_{CC} >V_{SS} must be maintained.

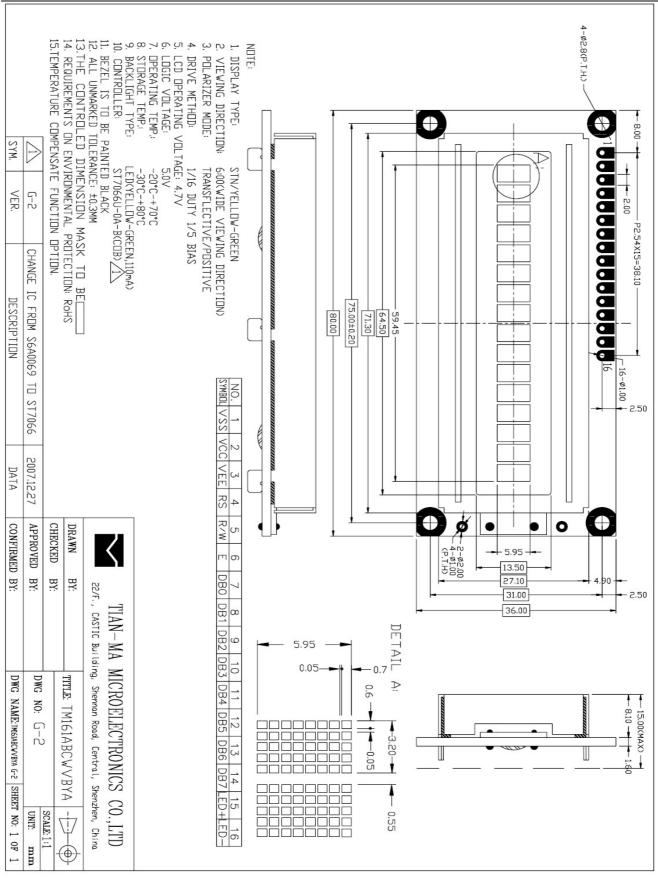
4 Mechanical Characteristics

4.1 Mechanical features

Parameter	Standard Value	Unit
Display type	Character Module	
Character size (W×H)	3.20 × 5.95	mm
Number of dots/characters (W×H)	16×1 (5×8)	
View area (W×H)	64.50 × 13.50	mm
Active Area (W×H)	59.45 × 5.95	mm
Dot Size (W×H)	0.60 × 0.70	mm
Dot Pitch (W×H)	0.65 × 0.75	mm
Module size (W×H×D)	80.00 × 36.00 × 15.00 (MAX)	mm
Module total weight (approx.)	35	g
Module outline dimensions	Refer to page 5-"Mechanical drawing"	

4.2 Mechanical drawing

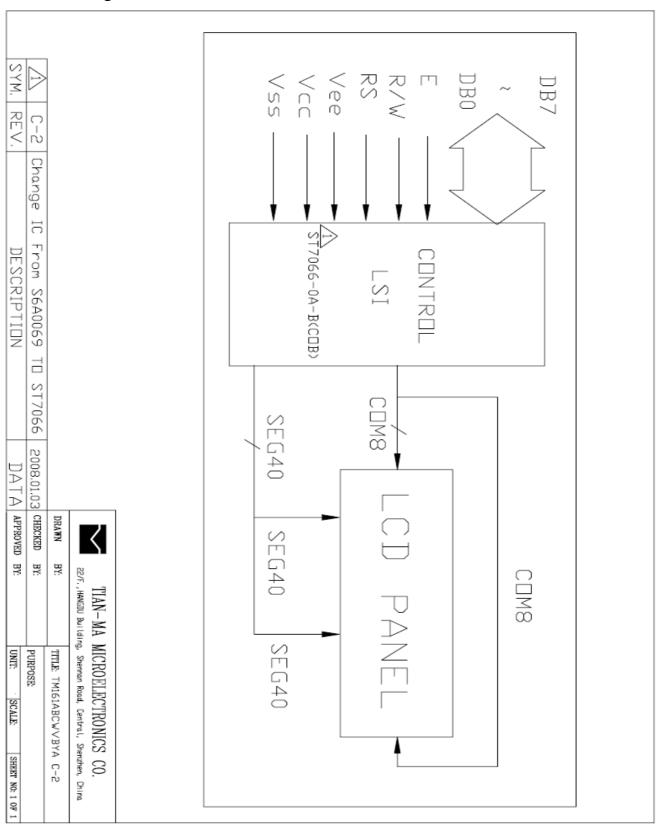






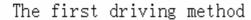
5 Circuit

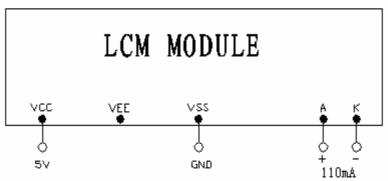
5.1 Block Diagram



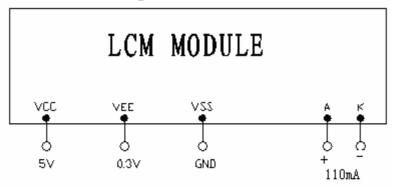


5.2 Recommend power supply circuit

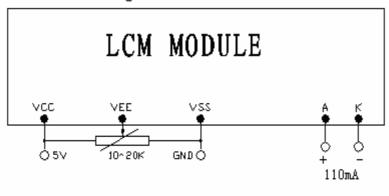




The second driving method



The third driving method



Note:

- You can control the contrast of module outside by add a VR (the third driving method, please remove R11 on PCB)
- You can use fixed current or 4.2V DC to drive the backlight

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7 / 29



6 Interface description

Symbol	I/O	Description
VSS	0V	Ground
VCC	5.0V	Power supply voltage for logic and LCD (+)
VEE	0.3V	Power supply voltage for LCD (-)
RS	I/O	Selects registers (H: Data L: Instruction)
R/W	I/O	Selects read or write
E	I/O	Data read/write enable signal
DB0	I/O	Data bit0
DB1	I/O	Data bit1
DB2	I/O	Data bit2
DB3	I/O	Data bit3
DB4	I/O	Data bit4
DB5	I/O	Data bit5
DB6	I/O	Data bit6
DB7	I/O	Data bit7
Α	440 4	Power supply voltage for LED (+)
K	TTUMA	Power supply voltage for LED (-)
	VSS VCC VEE RS R/W E DB0 DB1 DB2 DB3 DB4 DB5 DB6 DB7 A	VSS 0V VCC 5.0V VEE 0.3V RS I/O R/W I/O E I/O DB0 I/O DB1 I/O DB2 I/O DB3 I/O DB4 I/O DB5 I/O DB6 I/O DB7 I/O A 110mA



7 Instruction Code & Timing characteristics

7.1 COMMAND

The module TM161ABCWVBYA includes the controller-ST7066U. The table below lists the types of commands, including the code of each command. More details refer to ST7066U data sheet please.

,						nd C					to 3170000 data sheet plea	Execution	
Command	R	R/	DB	DB	DB	DB	DB	DB	DB	DB	Command Description	time (fsoc=	
	S	W	7	6	5	4	3	2	1	0		270kHz)	
Clear											Write "20H" to DDRAM. and set		
Display	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms	
Display											AC.		
											Set DDRAM address to "00H"		
Return											from AC and return cursor to its		
Home	0	0	0	0	0	0	0	0	1	Х	original position if shifted. The	1.52ms	
Home											contents of DDRAM are not		
											changed.		
											Sets cursor move direction and		
Entry Mode	0	0	0	0	0	0	0	1	I/D	S	specifies display shift. These	37us	
Set		U	0	U	U		U	'	1/0	٦	operations are performed during	37US	
											data write and read.		
Display											D=1:entire display on		
ON/OFF	0	0	0	0	0	0	1	D	С	В	C=1:cursor on	37us	
Control											B=1:cursor position on		
Cursor or											Set cursor moving and display		
Display Shift	0	0	0	0	0	1	S/C	R/L	Х	Х	shift control bit, and the direction,	37us	
Display Office											without changing DDRAM data.		
											Set interface data length (DL: 4-		
Function Set	0	0	0	0	1	DL	N	F	Х	х	bit/8-bit), numbers of display line	37us	
T dilottori oct					'		14				(N: 1-line/2-line), display font	0743	
											type(F: 5 X 8 dots/ 5 X 11 dots)		
Set CGRAM	0	0	0	1	AC	AC	AC	AC	AC	AC	Set CGRAM address in address	37us	
Address	Ľ		Ů	·	5	4	3	2	1	0	counter.		
Set DDRAM	0	0	1	AC	AC	AC	AC	AC	AC	AC	Set DDRAM address in address	37us	
Address	Ľ			6	5	4	3	2	1	0	counter.		
Read Busy											Whether during internal operation		
Flag and	0	1	BF	AC	AC	AC	AC	AC	AC	AC	or not can be known by reading	0us	
Address		·		6	5	4	3	2	1	0	BF. The contents of address		
7.00.000											counter can also be read.		
Write Data to	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM	37ms	
RAM				_ •							(DDRAM/CGRAM).		
Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM	37ms	
from RAM]								(DDRAM/CGRAM).	3.1110	



Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066U.If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to instruction Table for the list of each instruction execution time.

7.2 Interface Timing characteristics

Note: Please refer to IC: <u>ST7066U</u> data sheet for more details.

(VCC = 5V, TA = 25 %)

Mode	Characteristics	Symbol	Min	Тур	Max	Unit
	Enable Cycle Time	T _C	1200	ı	1	
	Enable Rise / Fall Time	T_R, T_F	-	-	25	
Write Mode (refer to Figure-1)	Enable Pulse Width	T_PW	140	-	-	
	Address Setup Time	T _{AS}	0	-	-	ns
	Address Hold Time	T_AH	10	-	-	
	Data Setup Time	T_{DSW}	40	-	-	
	Data Hold Time	T _H	10	-	-	

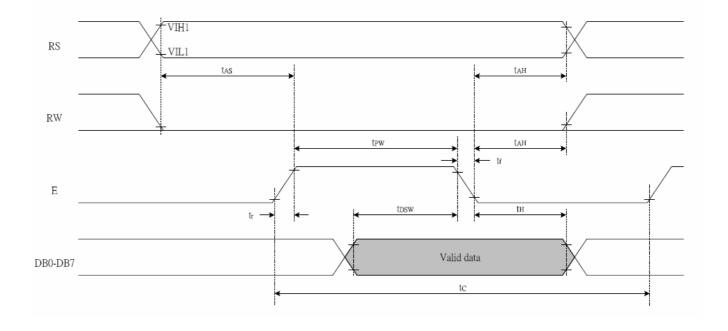


Figure 1: Write Mode Timing Diagram

30HS

Mode	Characteristics	Symbol	Min	Тур	Max	Unit
	Enable Cycle Time	T _C	1200	-	-	
	Enable Rise / Fall Time	T_R, T_F	-		25	
Read Mode	Enable Pulse Width	T_PW	140	-	-	
(refer to	Address Setup Time	T _{AS}	0	-	-	ns
Figure-2)	Address Hold Time	T _{AH}	10	-	-	
	Data Setup Time	T_{DDR}	-	-	100	
	Data Hold Time	T _H	10	-	-	

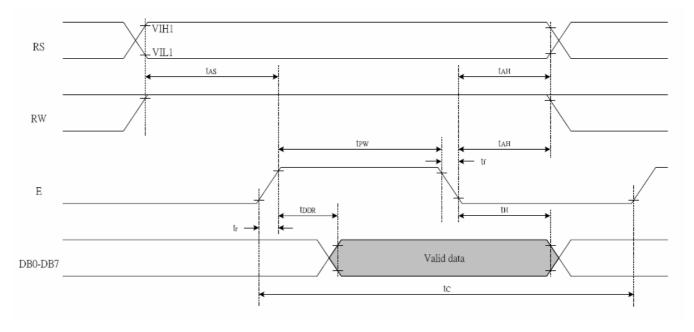


Figure 2: Read Mode Timing Diagram



7.3 character generator code map (Please refer to ST7066U datasheet for other character code map)

67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	G AM€															
0001	(2)															
0010	(3)															
0011	(4)															
0100	(5)															
0101	(6)															
0110	(7)															
0111	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)															
1110	(7)															
1111	(8)															

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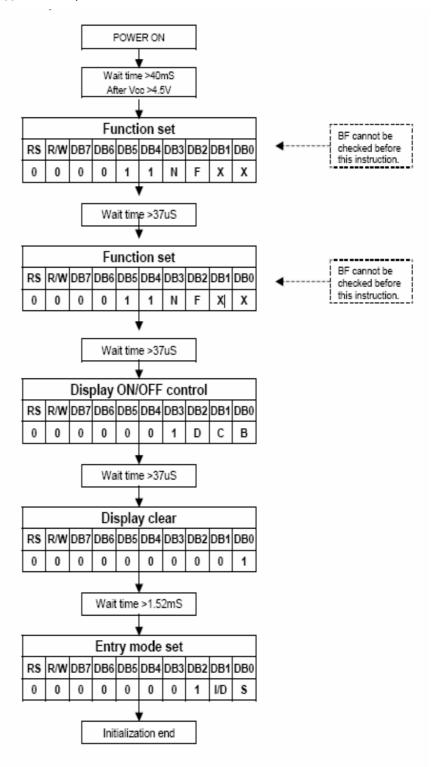
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12 / 29



7.4 Initialization flow map

(For 8-Bit Interface F_{OSC}=270 kHz)





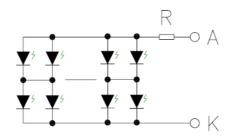
8 Electrical characteristics

 $V_{CC}=5.0V$, $V_{SS}=0V$, Ta=25 °C

Parameter		Symbol	Condition	MIN	TYP	MAX	UNIT	
Logic circuit supply voltage		V _{CC}		4.8	5.0	5.2		
Power supply LCD		V _{LCD}	VCC-VEE	4.4	4.7	5.0		
Input voltage for logic circuit	"H"level	V _{IH}		0.7V _{CC}		V _{CC}	v	
	"L"level	V _{IL}		-0.3		0.6	V	
Output voltage for	"H"level	V _{OH}	I _{OH} =-0.1mA	3.9				
logic circuit	"L"level	V _{OL}	I _{OL} =0.1mA			0.4		
Logic power supply current (Without backlighting)		Icc	F _{OSC} =270KHz VCC=5.0V		0.5	2.0	mA	
Used driver IC		ST7066U OF SITRONIX						

9 LED backlight characteristics

Ta=25°C



CIRCUIT DIAGRAM (LED 2X11=22 dies)

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Forward voltage	V_{f}	I _f =110mA	4.0	4.2	4.4	V
Luminous intensity*	Вр		150	200		cd/m ²
Luminous Uniformity*	△Вр	I _f =110mA	70			%
Peak Wave length	λр		569	572	575	nm

Note:

- Measured at the bare LED backlight unit.
- If the backlight is above these maximum ratings for long time, the service life of the LED backlight will reduce or it will cause poor reliability.

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14 / 29



10 Optical Characteristics

10.1 Optical Characteristics

Ta=25 ℃

Parar		Cymbol		Ratings		Unit	Measuring	Reference	
raiai	neter	Symbol	Min	Type	Max.	Uffil	Temp.	rtelefence	
Operatino	g voltage	V _o	4.5	4.7	4.9	V	25℃	(Note10-1)	
Frame from	equency	f		70		Hz	25℃	(Note10-2)	
Contras	st ratio	Cr(<i>θ</i> =20°, <i>Φ</i> =90° or 270°)		10			25℃	(Note10-3)	
	Turn on	+			250	ms	25℃		
Response		t _{on}	1			1115	0℃	(Note10-4)	
time	Turn off	+	1		250	ms	25℃	(Note 10-4)	
	Tullion	t _{off}				1115	0℃		
Viewing angle	Up-down	<i>θ</i> 1 (<i>Φ</i> =90° or 270°)	-45-60 -35-40			deg	25℃	(Note10-5)	
arigle (Cr≥2)	Left-right	<i>θ</i> 2 (<i>Φ</i> =0° or 180°)				deg	25℃	(1401610-3)	

(Note10-1) The maximum and minimum ratings don't mean the LCD works well in the whole range of Vo. Vo must be adjusted to optimize the viewing angle and contrast. Refer to definition of drive voltage, refer to 10.2.

(Note10-2) The frequency shouldn't be too low to avoid flicker. Refer to definition of drive voltage, refer to 10.2.

(Note10-3) Refer to 10.2/10.3/10.4/10.5.

(Note10-4) The selected state is dark and non-selected state is white (or bright) with positive type, reversely the selected state is white (or bright) and non-selected state is dark with negative type. Refer to 10.6 definition of response time.

(Note10-5) Generally the viewing direction is 6:00 or12:00, sometimes 3:00 or 9:00. The range of left to right and up to down based on Cr=2 show the viewing angle. Viewing angle range isn't the range of defects inspection. Refer to 10.4.



10.2 Definition of drive voltage

(1) Definition of drive voltage and waveform

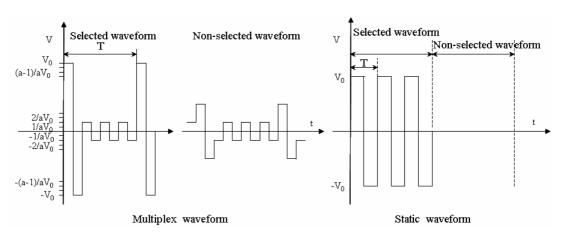


Fig.1: Definition of drive voltage and waveform

Operating voltage: V_o Frame frequency: f=1/T

Duty: 1/N Bias: 1/a

(2) Operating voltage: Vo

TIANMA can evaluate whether the LCD can be redesigned to obtain customer preferable performance if customer's LCD drive voltage isn't adjustable.

10.3 Optical characteristics measurement equipment and method

The setup and test method are showed in fig.2. Test methods are different according to different illumination mode.

Transmissive mode: light resource is placed at the back of LCD.

Reflective mode and transflective mode: light resource is placed at the front side of LCD.

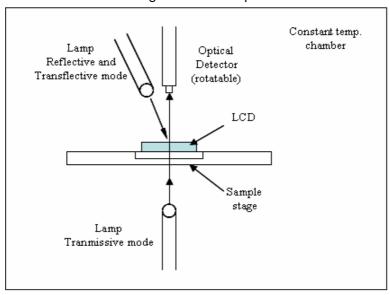


Fig.2: Optical characteristics measurement equipment

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The chamber temperature, light resource and driving signal should be stable before testing. If test the characteristics under high or low temperature, the test system should be stable for more than 10 minutes before testing.

10.4 Definition of viewing direction

Refer to the graph below marked by θ and Φ

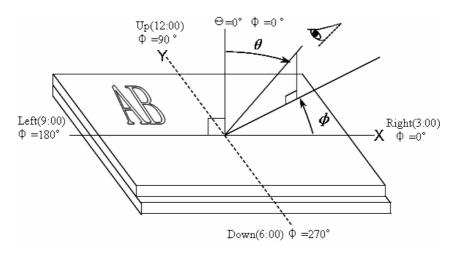


Fig.3: Definition of viewing direction

10.5 Definition of contrast ratio

Contrast ratio can be calculated by the formula (10-1) below for positive type. If the LCD is negative type, $Cr(\theta, \Phi)$ is equal to luminance $(\theta, \Phi, \text{non-selected state})$ divided by luminance $(\theta, \Phi, \text{selected state})$. Note3-4 shows the relationship between selected state, non-selected state and bright state, dark state.

$$\operatorname{Cr}(\theta, \phi) = \frac{L_2}{L_1} = \frac{\operatorname{Luminance}(\theta, \phi) \text{ (Bright state)}}{\operatorname{Luminance}(\theta, \phi) \text{ (Dark state)}}$$
(10-1)

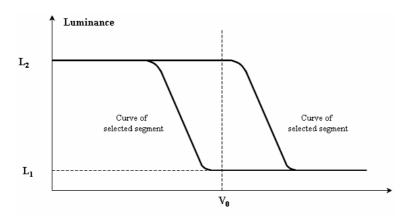


Fig.4: Electro-optical characteristic (EOC) graph (positive type)

10.6 Definition of response time

Turn on time (rise time): $t_{on} = t_d + t_r$ (from non-selected state to selected state) Turn off time (fall time): $t_{off} = t_D + t_R$ (from selected state to non-selected state)

30HS

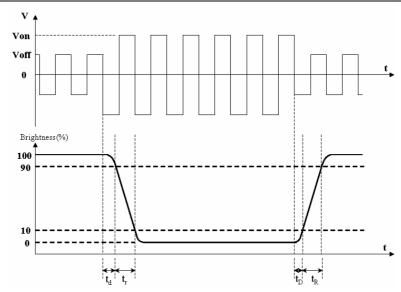


Fig.5: Definition of response time (positive type)

10.7 Definition of viewing angle

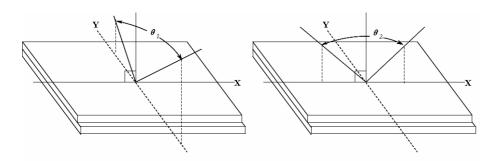


Fig 6: Definition of viewing angle

 θ_1 ——range of viewing angle from up to down

 θ_2 ——range of viewing angle from left to right.



11 Reliability

11.1 Content of Reliability Test

Ta=25°C

No	Test Item	Test condition	Criterion			
1	High Temperature Storage	80 °C±2 °C 120H Restore 2H at 25 °C Power off				
2	Low Temperature Storage	-30 °C±2 °C 120H Restore 2H at 25 °C Power off				
3	High Temperature Operation	70 °C±2 °C 120H Restore 2H at 25 °C Power on				
4	Low Temperature Operation	-20 °C±2 °C 120H Restore 4H at 25 °C Power on	After testing, cosmetic and electrical defects			
5	High Temperature & Humidity Operation	nidity Operation 40 °C±2 °C 90%RH 120H Power on				
6	Temperature Cycle -30 ℃ →25 ℃ →80 ℃ 30min 5min 30min after 10cycles, Restore 2H Power off					
7	Vibration Test	ration Test 10Hz~150Hz, 100m/s², 120min				
8	Shock Test	Half-sine wave,300m/s ² ,11ms				
9	Drop Test (package state)	800mm, concrete floor,1corner, 3edges, 6 sides each time	1.After testing, cosmetic and electrical defects should not happen. 2.The product should remain at initial place 3.Product uncovered or package broken is not permitted.			

Notes:

- 1. Each test item applies for a test sample only once, the test sample can not be used again in any other test item
- 2. The test sample is inspected after 2 hours or more storing at room temperature and room humidity after each test item is finished.
- 3. The criteria refer to 11.2.



11.2 Inspection of criteria

Remark NO.	Content
1	Functional test is OK. Missing Segment, shorts, unclear segment, non-display, display abnormally, liquid crystal leak are unallowable.
2	After testing, cosmetic defects should not happen, no low temperature bubbles, seal loose and fall, frame rainbow, ACF bubble growing are unallowable in the appearance test.
3	Total current consumption should not be over 10% of initial value.
4	After tests being executed, Contrast must be larger than 70% of its initial value prior to the tests.
5	No glass crack, chipped glass, end seal loose frame crack and so on.
6	No structure loose and fall.



12 Quality level

12.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially degrade usability for product applications, including all functional defects (such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

12.2 Definition of inspection range

For LCD defects, dividing two areas to make a judgment (according figure 1).

A zone : Inside Viewing area B zone : Outside Viewing area

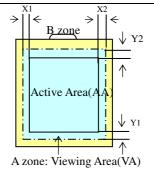


Figure 1

12.3 Inspection items and general notes

12.3 inspection items and general notes									
General notes	 ②Viewing area should be the a ③Limit sample should be prior ④Viewing judgment should be ⑤Inspection conditions Inspection distance: 250 mm 	Inspection distance: 250 mm (from the sample) Temperature : 25±5 °C Inspection angle: 45 degrees in 12 o'clock direction (all defects in viewing area should be inspected from this direction)							
	Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble								
	Contrast variation The color of a small area is different from the remainder. phenomenon changes with voltage								
Inspection items	Polarizer defect	Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass							
	Functional defect	no display, display abnormally, open or missing segment, short circuit, False viewing direction							
	Glass defect	Glass crack, Shaved corner of glass, Surplus glass							
	Segment defect	Pin holes or cracks in segment, Transformation of segment							
	PCB defect	Components assembly defect							

30HS



12.4 Outgoing Inspection level

Outgoing Inspection	Inspection conditions		Inspection						
standard	inspection conditions	Min.	Max.	Unit	⊒	AQL			
Major Defects	See 13.3 general notes	S	ee 13.	5		0.65			
Minor Defects	See 13.3 general notes See 13.5				Ш	1.5			
Note: Sampling standa	rd conforms to GB2828								

12.5 Inspection Items and Criteria

				Judgmer	nt standard		
	Inspection	on items		Category	Acceptable r	number	
				Calegory	A zone	B zone	
	Black spot, white spot, Bright Spot,	pot, Bright Spot, () b _		Ф≦0.10	Neglected		
1	Pinhole, Foreign Particle, Particle in or on glass,	a		0.10<Φ≦0.20	3	Neglected	
	Scratch on glass	Φ=(a+b)/2(mm)	С	0.20<Ф	0		
	Black line, White	A	Α	W≦0.02	Neglected		
2	line, Particle Between Polarizer and glass, Scratch		В	0.02 <w 0.05<br="" ≦="">L ≦ 3.0</w>	3	Neglected	
	on glass	n glass L: Length(mm)		W>0.05 or L>3.0	0		
				Ф≦0.2	Neglected		
		b	В	0.2<Φ≦0.3	2	- Neglected	
3	Contrast variation	uriation a		0.3<Φ≦0.4	1	Neglected	
		Φ=(a+b)/2(mm)	D	0.4<Ф	0		
			То	tal defective point(B,C)	3		
4	Bubble inside cell			any size	none	none	
5	Polarizer defect (if Polarizer is used)	Scratch, damage on polarizer, particle on polarizer or between polarizer and glass.	Re	fer to item 1 and item 2.			
		Bubble, dent and convex	Α	Ф≦0.3	Neglected	Neglecte d	
		COLIVEX	В	0.3<Φ≦0.7	2	u	

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22 / 29



		es Co.,LID	т т					
			С	0.7<Ф		0		
	Complete	Stage surplus glass	b≦(0.3mm				
6	Surplus glass	Surrounding surplus glass	Should not influence outline dimension and assembling.					
7	Open segment or op	pen common	Not	permitted				
8	Short circuit		Not	permitted				
9	False viewing direct	ion	Not	permitted				
10	Contrast ratio uneve	en	Acc	ording to the limit	specin	nen		
11	Crosstalk	According to the limit specimen		According to the limit specimen				
12	Black /White spot(di	splay)	Refer to item 1					
13	Black /White line(dis	splay)	Ref	er to item 2				
14		- + - a			Ма	x.3 dots allowed		
	Pin holes and			x<0.1mm	0.	1mm≤x≤0.2mm		
	cracks in segment	a-1 F-		X=((a+b)/2		Max.3 dots	
		-D		not counted	Max.2 dots allowed each segment		allowed	
				A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm			
15	Transformation of segment			not counted		.1 defect allowed each segment		
				x<0.1mm	0.	1mm≤x≤0.2mm		
		x=(a+b)/2			Max.3 defects			
		D-++a		not counted		.1 defect allowed each segment	allowed	
				a<0.1mm	0.	1mm≤a≤0.2mm D>0		





0.8W≤a≤1.2W a=measured value of width W=nominal value of width

Max.2 defects allowed

				Judgment standard	
		Inspection items		Category(application: B zone)	Acceptable number
16	Glass defect crack	①The front of lead terminals b c		a≤ t, b≤1/5W, c≤3mm Crack at two sides of lead terminals should not cover patterns and alignment mark	Max.3 defects allowed
		②Surrounding crack—non-contact side seal c	b <	Inner borderline of the seal	
		3 Surrounding crack— contact side seal c b a Inner border line of the seal Outer border line of the seal	b <	< Outer borderline of the seal	



@Corner	Α	$a \le t$, $b \le 3.0$, $c \le 3.0$	
w b c	В	Glass crack should not cover patterns u and alignment mark and patterns.	

Inspection items		Inspection items	Judgment standard		
			Category(application: B zone)		
17	PCB defect	Component soldering: No cold soldering, short, open circuit, burr, tin ball The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1); the sheet component deviation: Pin deviates from the pad and contact with the near components is not permitted (Pic.2) lead defect: The lead lack must be less than 1/3 of its width; The lead burr must be less than 1/3 of the seam; Impurities connect with the near leads is not permitted	Component Soldering pad Lead L1>0 L2>0		



Microel	ectronics Co.,LTD	MODULE No.: IM161ABCWVBYA V1.0
	Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald	head Base Board Soldering tin is not permit in this area
	Serious cave distortion on plug and socket contact pin is not permitted	Soldering tin is not permit in this area
		socket Base Board
	Glue on root of the speaker receiver and motor lead: The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.	Glue Lead PCB Insulative coat



13 Precautions for Use of LCD Modules

13.1 Handling Precautions

- 13.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 13.1.2 Liquid in LCD is hazardous substance, if the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, thoroughly and promptly wash it off using soap and water.
- 13.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 13.1.4 Don't touch, push or rub the exposed polarizer covering the display surface of the LCD module with anything harder than an HB pencil lead, the polarizer is soft and easily scratched, handle it carefully.
- 13.1.5 Don't put or attach anything on the display area to avoid leaving any marks on.
- 13.1.6 If the display surface is contaminated or becomes dusty, breathe on the surface and gently wipe it with a soft dry cloth. Do not scrub hard to avoid damage of the surface. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 13.1.7 Do not attempt to disassemble the LCD Module.
- 13.1.8 If the logic circuit power is off, do not apply the input signals.
- 13.1.9 Avoid using the same display pattern long time (continuous ON segment). Software must be prepared so that the pattern will be changed
- 13.1.10 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body and electric appliances when handling the LCD Modules. It is preferable to use conductive mat on table and wear cotton clothes or conductive processed fiber. Synthetic fiber is not recommended.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be careful and slow when peeling off this protective film since static electricity may be generated. It is recommended to use ionic fan or machine when operating. It is recommended to remove the protection foil slowly (> 3 sec.).
 - e. It is preferable to wear gloves etc, to avoid damaging the LCD. Please do not touch electrodes with bare hands or avoid any other contamination.



13.2 Storage precautions

- 13.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 13.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $5 \,^{\circ}\text{C} \sim 40 \,^{\circ}\text{C}$ Relatively humidity: $\leq 80\%$

- 13.2.3 The LCD modules should be stored in a clean environment or room, free from acid, alkali and harmful gas.
- 13.2.4 Store the module in anti-static electricity container and without any physical load.

13.3 Transportation precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

13.4 Soldering

- 13.4.1 Use the high quality solders, only solder the I/O terminals.
- 13.4.2 No higher than 280 □ and time less than 3-4 second during soldering.
- 13.4.3 Rewiring: no more than 3 times.
- 13.4.4 When you remove connector or cable soldered to I/O terminals, please confirm that solder is fully melted. If you remove by force, electrodes at I/O terminals may be damaged (or stripped off). It is recommended to use solder suction machine.



14. LCD Module Part Numbering System

TM	161	Α	В	С	W	V	В	Υ	Α

1	2	3	(4)	(5)	<u>6</u>	\bigcirc	(2)	(110
(I)	((S)	4	\odot	(b)	\mathcal{O}	o	Θ	Œ

NO.	Explanation						
1	TIANMA module indicating						
2	Module type: 3 DIGITS, 16-Character × 1-Line						
3	TIANMA module series (A,B,C,D)						
4	LCD t	уре					
4)	В	Positive, Yellow-Green mode, STN					
(5)	Backl	ight type					
9	С	Transflective, LED					
6	Temperature range						
0	W	Wide temperature					
7	Viewing Angle						
	V	Wide viewing direction					
8	Technology						
0	B COB (including SMT)						
9	The color of backlight						
9)	Υ	Yellow-green					
(10)	Function choice						
(10)	Α	Without any function					

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