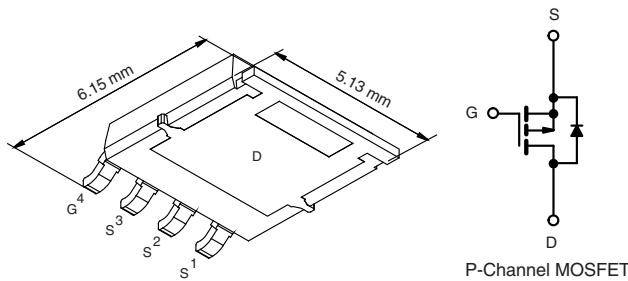


Automotive P-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	- 40
$R_{DS(on)}$ (Ω) at $V_{GS} = - 10$ V	0.010
$R_{DS(on)}$ (Ω) at $V_{GS} = - 4.5$ V	0.015
I_D (A)	- 30
Configuration	Single

PowerPAK® SO-8L Single



FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC
- AEC-Q101 Qualified^d
- Find out more about Vishay's Automotive Grade Product Requirements at: www.vishay.com/applications



RoHS
COMPLIANT
HALOGEN
FREE

ORDERING INFORMATION	
Package	PowerPAK SO-8L
Lead (Pb)-free and Halogen-free	SQJ463EP-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	- 40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^a	$T_C = 25$ °C	I_D	- 30	A
	$T_C = 125$ °C		- 30	
Continuous Source Current (Diode Conduction) ^a		I_S	- 30	
Pulsed Drain Current ^b		I_{DM}	- 120	
Single Pulse Avalanche Current	$L = 0.1$ mH	I_{AS}	- 44	mJ
Single Pulse Avalanche Energy		E_{AS}	97	
Maximum Power Dissipation ^b	$T_C = 25$ °C	P_D	83	W
	$T_C = 125$ °C		28	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to + 175	°C
Soldering Recommendations (Peak Temperature) ^{e, f}			260	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	65	°C/W
Junction-to-Case (Drain)		R_{thJC}	1.8	

Notes

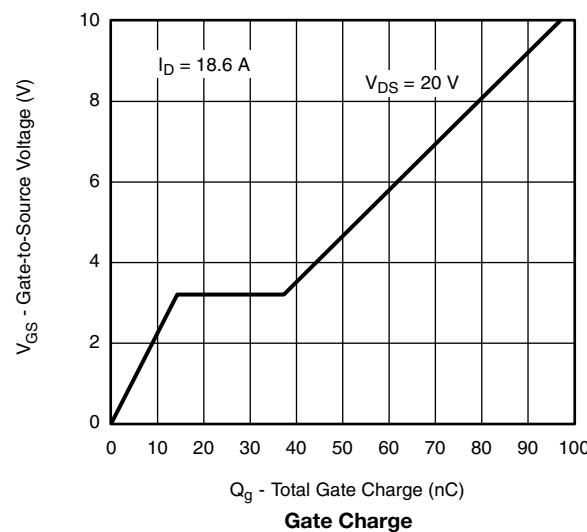
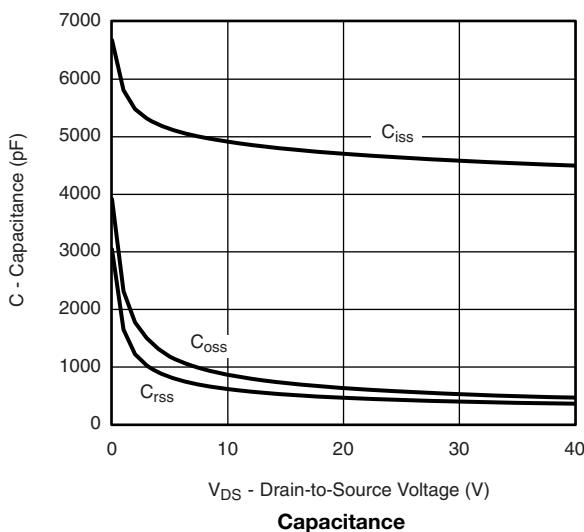
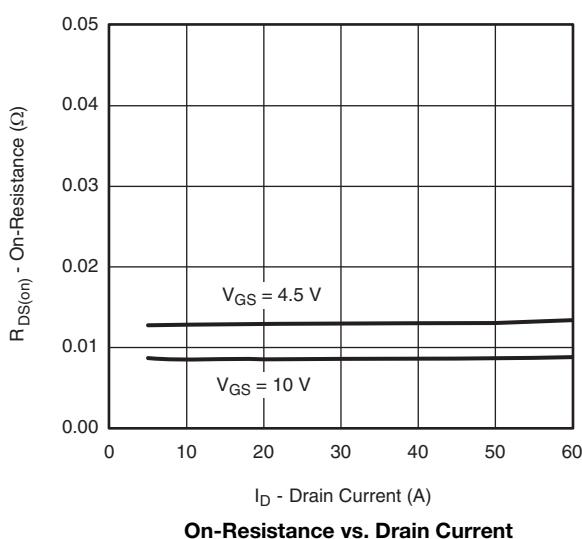
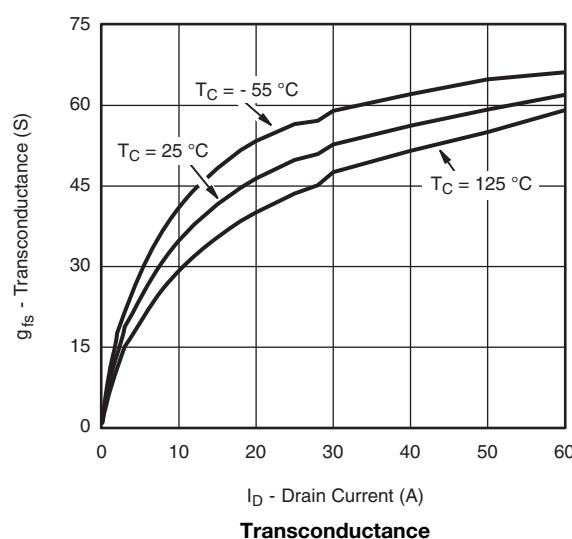
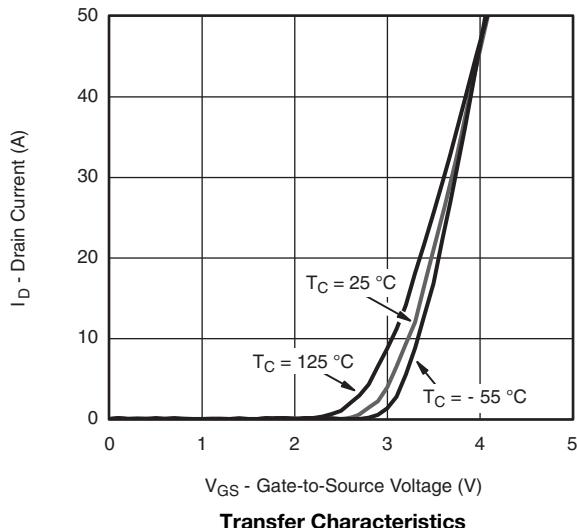
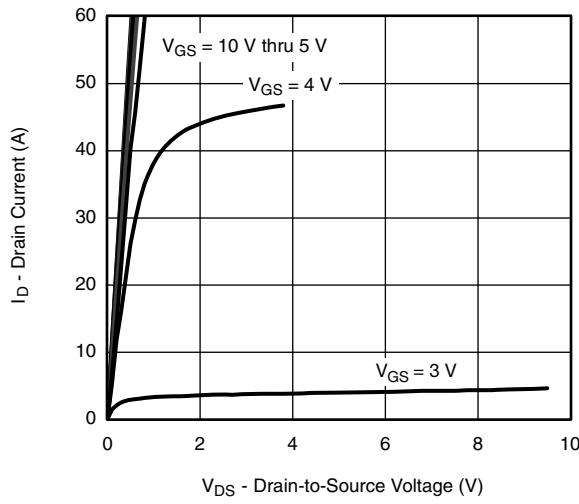
- Package limited.
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.
- See Solder Profile (www.vishay.com/ppg?73257). The PowerPAK SO-8L. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

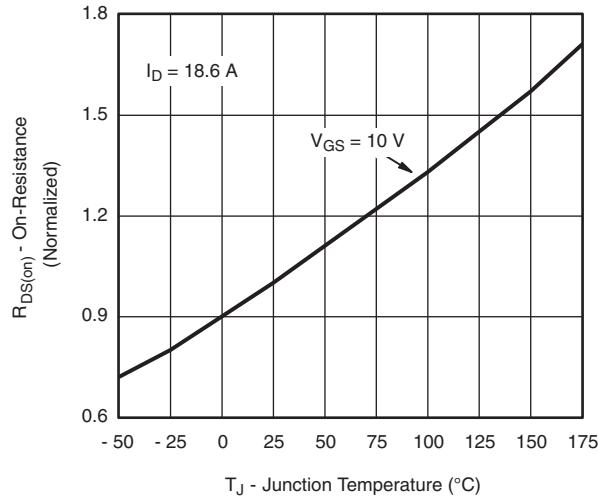
SPECIFICATIONS ($T_C = 25^\circ\text{C}$, unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}$, $I_D = - 250 \mu\text{A}$		- 40	-	-	V	
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = - 250 \mu\text{A}$		- 1.5	- 2.0	- 2.5		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0 \text{ V}$	$V_{DS} = - 40 \text{ V}$	-	-	- 1	μA	
		$V_{GS} = 0 \text{ V}$	$V_{DS} = - 40 \text{ V}$, $T_J = 125^\circ\text{C}$	-	-	- 50		
		$V_{GS} = 0 \text{ V}$	$V_{DS} = - 40 \text{ V}$, $T_J = 175^\circ\text{C}$	-	-	- 150		
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{GS} = - 10 \text{ V}$	$V_{DS} \leq - 5 \text{ V}$	- 30	-	-	A	
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = - 10 \text{ V}$	$I_D = - 18 \text{ A}$	-	0.008	0.010	Ω	
		$V_{GS} = - 4.5 \text{ V}$	$I_D = - 15 \text{ A}$	-	0.012	0.015		
		$V_{GS} = - 10 \text{ V}$	$I_D = - 18 \text{ A}$; $T_J = 125^\circ\text{C}$	-	0.013	0.015		
		$V_{GS} = - 10 \text{ V}$	$I_D = - 18 \text{ A}$; $T_J = 175^\circ\text{C}$	-	0.015	0.018		
Forward Transconductance ^b	g_{fs}	$V_{DS} = - 15 \text{ V}$, $I_D = - 18 \text{ A}$		-	45	-	S	
Dynamic^b								
Input Capacitance	C_{iss}	$V_{GS} = 0 \text{ V}$	$V_{DS} = - 20 \text{ V}$, $f = 1 \text{ MHz}$	-	4700	5875	pF	
Output Capacitance	C_{oss}			-	630	790		
Reverse Transfer Capacitance	C_{rss}			-	460	575		
Total Gate Charge ^c	Q_g	$V_{GS} = - 10 \text{ V}$	$V_{DS} = - 20 \text{ V}$, $I_D = - 18.6 \text{ A}$	-	98	150	nC	
Gate-Source Charge ^c	Q_{gs}			-	14	-		
Gate-Drain Charge ^c	Q_{gd}			-	23	-		
Turn-On Delay Time ^c	$t_{d(\text{on})}$	$V_{DD} = - 20 \text{ V}$, $R_L = 20 \Omega$ $I_D \equiv - 1 \text{ A}$, $V_{GEN} = - 10 \text{ V}$, $R_g = 6.0 \Omega$	$V_{DS} = - 20 \text{ V}$, $I_D = - 18.6 \text{ A}$	-	21	32	ns	
Rise Time ^c	t_r			-	17	26		
Turn-Off Delay Time ^c	$t_{d(\text{off})}$			-	121	182		
Fall Time ^c	t_f			-	51	77		
Source-Drain Diode Ratings and Characteristics^b								
Pulsed Current ^a	I_{SM}			-	-	- 120	A	
Forward Voltage	V_{SD}	$I_F = - 4.5 \text{ A}$, $V_{GS} = 0 \text{ V}$		-	- 0.8	- 1.2	V	

Notes

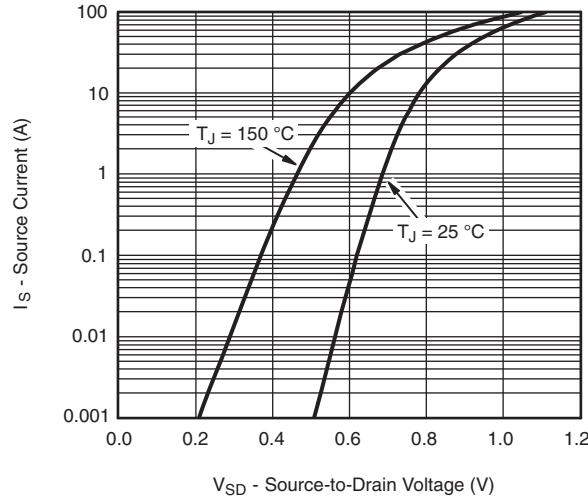
- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

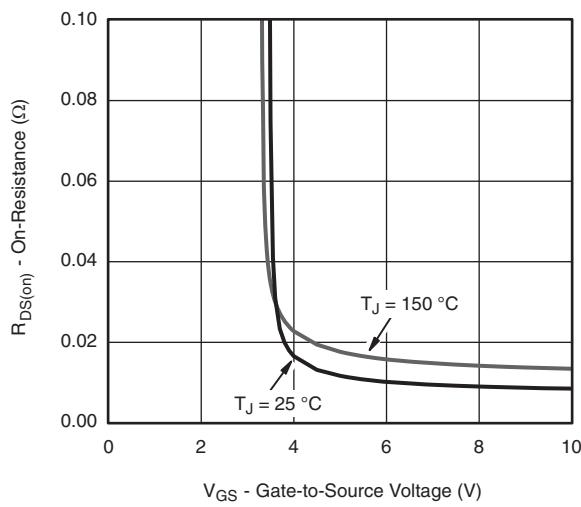
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)


TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

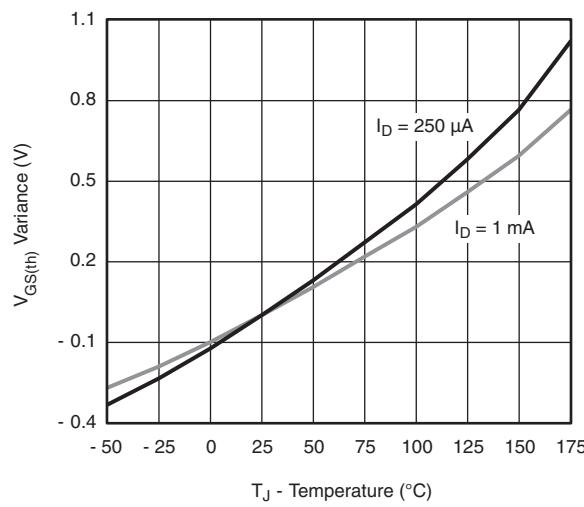
On-Resistance vs. Junction Temperature



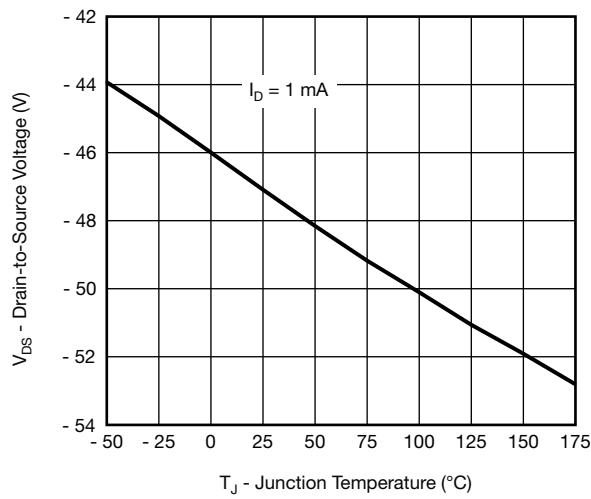
Source Drain Diode Forward Voltage



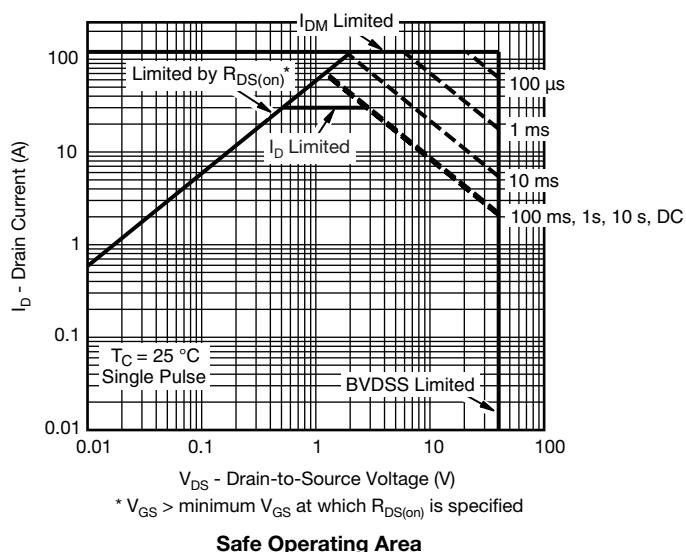
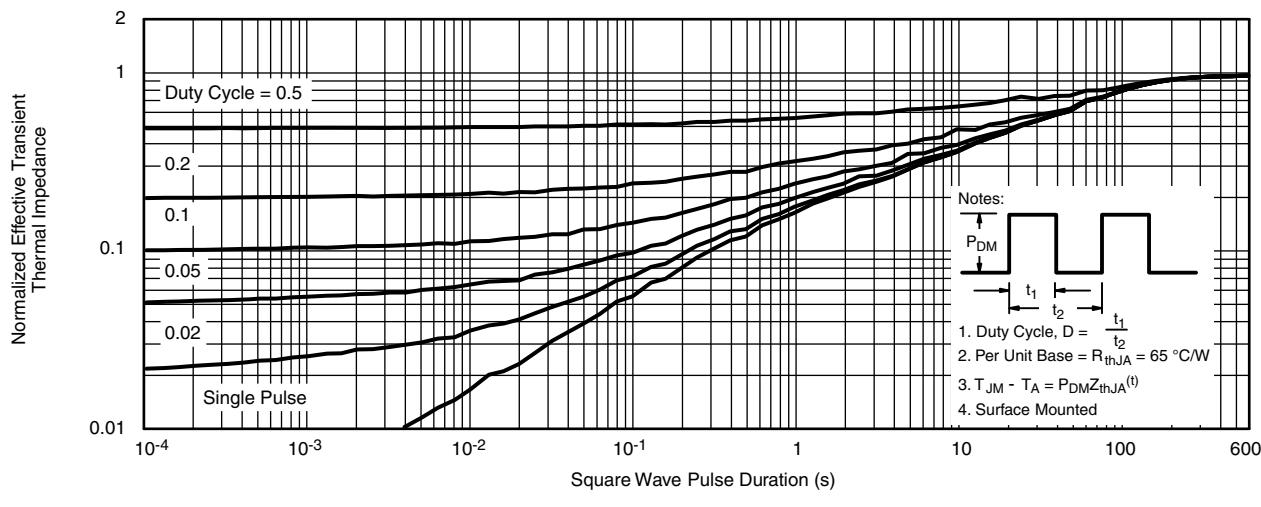
On-Resistance vs. Gate-to-Source Voltage

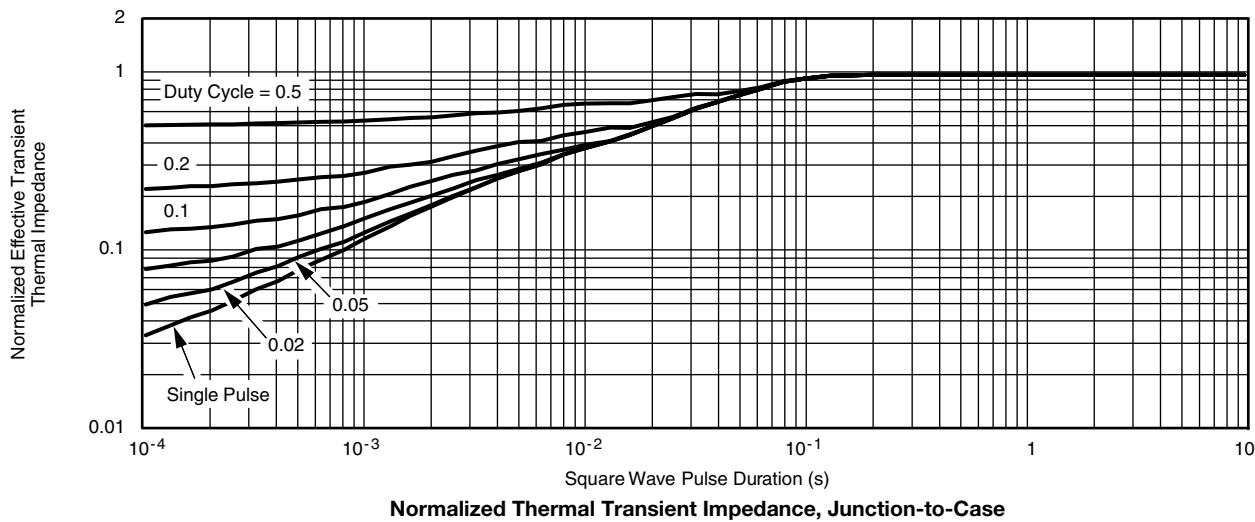


Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

THERMAL RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise noted)

Safe Operating Area

Normalized Thermal Transient Impedance, Junction-to-Ambient

THERMAL RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise noted)**Note**

- The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25°C)
- Normalized Transient Thermal Impedance Junction-to-Case (25°C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



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