

Winstar Display Co., LTD 華凌光電股份有限公司

住址: **407** 台中市中清路 **163** 號 No.163 Chung Ching RD., Taichune, Taiwan, R.O.C

CUSTOMED

WEB: http://www.winstar.com.tw E-mail: winstar@winstar.com.tw Tel:886-4-24262208 Fax: 886-4-24262207



SPECIFICATION

COSTOM					_		
MODULE	NO.:	WG12864E-WGH-TZ					
APPROVI	ED BY:						
(FOR CUSTOMER	R USE ONLY)						
		PCB VE	RSION:		DATA:		
SALES RV	APPROVED	PV	CHECKED	RV	PRFPARFN RV		

ISSUED DATE:		



MODLE NO :

DOC. FIRST ISSUE **RECORDS OF REVISION REVISED** SUMMARY **VERSION** DATE PAGE NO. 2005.12.30 First issue 0

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1. Module Classification Information

① Brand: WINSTAR DISPLAY CORPORATION

② Display Type: H→Character Type, G→Graphic Type

③ Display Font: 128 * 64 Dots + 4 Icon

Model serials no.

B→EL, Blue green A→LED, Amber

 $D \rightarrow EL$, Green $R \rightarrow LED$, Red

 $W \rightarrow EL$, White $O \rightarrow LED$, Orange

 $F \rightarrow CCFL$, White $G \rightarrow LED$, Green

Y→LED, Yellow Green B→LED, Blue

© LCD Mode : $B \rightarrow TN$ Positive, Gray $T \rightarrow FSTN$ Negative

N→TN Negative,

G→STN Positive, Gray

Y→STN Positive, Yellow Green

M→STN Negative, Blue

F→FSTN Positive

② LCD Polarizer Type/ A→Reflective, N.T, 6:00 H→Transflective, W.T,6:00

Temperature range/ D→Reflective, N.T, 12:00 K→Transflective, W.T, 12:00 View direction

 $G \rightarrow Reflective$, W. T, 6:00 $C \rightarrow Transmissive$, N.T,6:00

J→Reflective, W. T, 12:00 F→Transmissive, N.T,12:00

B→Transflective, N.T,6:00 I→Transmissive, W. T, 6:00

E→Transflective, N.T.12:00 L→Transmissive, W.T,12:00

Special Code
T: Temperature Compensation

Z:NT7086 Driver

2. Precautions in Use of LCD Module

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD Module.
- (3)Don't disassemble the LCM.
- (4)Don't operate it above the absolute maximum rating.
- (5)Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7)Storage :please storage in anti-static electricity container and clean environment.

3. General Specification

ITEM	STANDARD VALUE	UNIT
Number of dots	$128 \times 64 + 4 \text{ Icon}$	dots
Module dimension	54.0(W) ×50.0(H) ×7.5 max(T)	mm
View area	43.5(W) ×29.0(H)	mm
Active area	40.92(W) ×26.92(H)	mm
Dot size	$0.28(W) \times 0.35(H)$	mm
Dot pitch	$0.32(W) \times 0.39(H)$	mm
LCD type	STN, positive, transflective, gray	
View direction	6 o'clock	
Backlight	EL (white)	

4. Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNNIT
Operating Temperature	T_{OP}	-20	_	+70	$^{\circ}\!\mathbb{C}$
Storage Temperature	T_{ST}	-30	_	+80	$^{\circ}\!\mathbb{C}$
Input Voltage	V _I	0	_	$V_{ m DD}$	V
Supply Voltage For Logic	$V_{ m DD}$	0	_	6.7	V
Supply Voltage For LCD	V_{DD} - V_{LCD}	0	_	17	V

5. Electrical Characteristics

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage For Logic	V_{DD} - V_{SS}	_	2.8	5.0	5.5	V
		Ta=-20°C	_	_	9.8	V
Supply Voltage For LCD	V_{DD} - V_0	Ta=25°C	_	8.2	_	V
		Ta=70°C	6.7	_	_	V
Input High Vol	$V_{ m IH}$	_	2.0	_	V_{DD}	V
Input Low Vol	V_{IL}	_	0	_	0.8	V
Output High Vol	$ m V_{OH}$	_	2.4	_	$V_{ m DD}$	V
Output Low Vol.	V_{OL}	_	0	_	0.4	V
Power Supply Current	I _{DD} (EL OFF)		6.0	8.0	12.0	mA
Power Supply Current	I _{DD} (EL ON)		36.0	40.0	50.0	mA

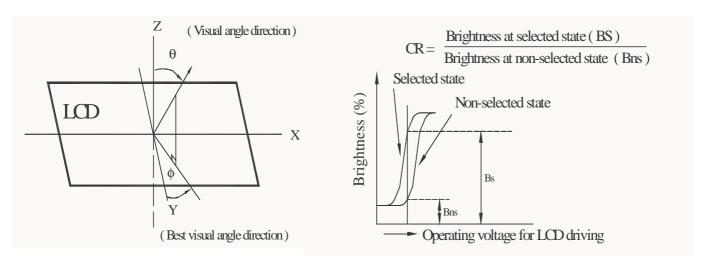
6. Optical Characteristics

ITEM	SYMBAL	CONDITION	MIN	TYP	MAX	UNIT
77' A 1	(V) θ	CR≧2	20		40	deg.
View Angle	(H) φ	CR≧2	-30		30	deg.
Contrast Ratio	CR	_	2	3		_
D (7)	T rise	_		135	270	ms
Response Time	T fall	_		265	400	ms

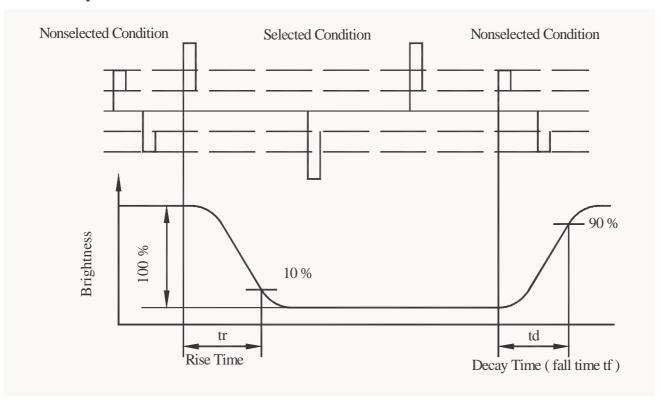
6.1 Definitions

■ View Angles

■Contrast Ratio



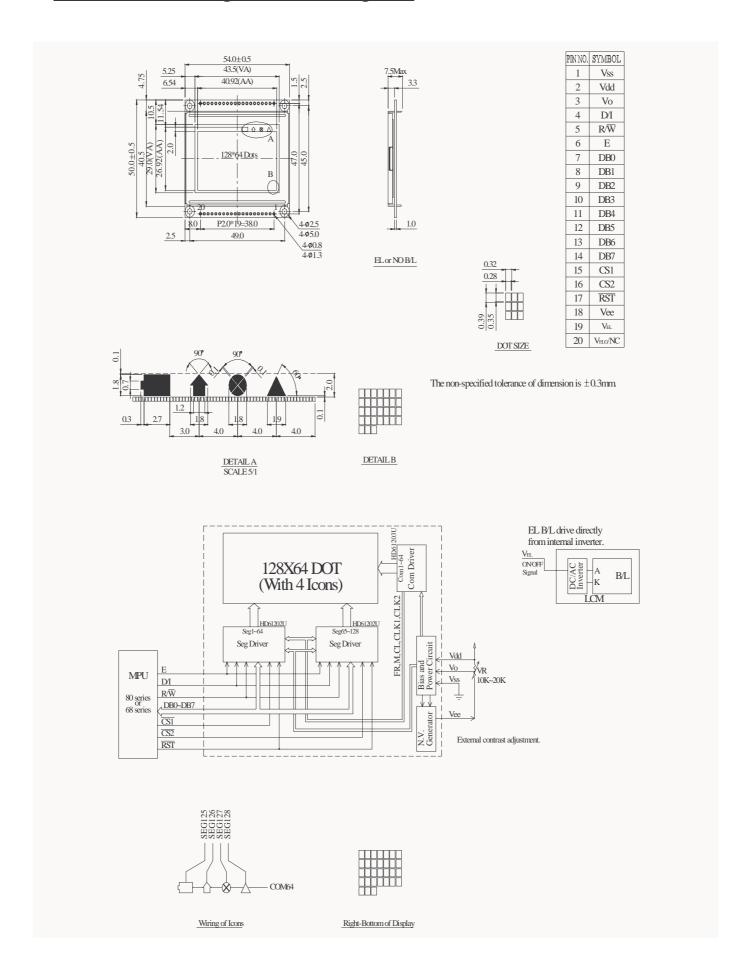
■Response time



7. Interface Pin Function

Pin No.	Symbol	Level	Description
1	Vss	0V	Power supply (0V)
2	V_{DD}		Power supply
3	Vo	(Variable)	Operating voltage for LCD driving
4	D/I	H/L	Register selection input High: Data register Low: Instruction register (for write) Busy flag address counter (for read)
5	$R/\overline{\overline{W}}$	H/L	R/W signal input is used to select the read/write mode
6	Е	Н	High=Read mode ,Low=Write mode Start enable signal to read or write the data
7	DB0	H/L	Data bus line
8	DB1	H/L	Data bus line
9	DB2	H/L	Data bus line
10	DB3	H/L	Data bus line
11	DB4	H/L	Data bus line
12	DB5	H/L	Data bus line
13	DB6	H/L	Data bus line
14	DB7	H/L	Data bus line
15	CS1	Н	Chip Enable (Select Column 1 ~ Column 64)
16	CS2	Н	Chip Enable (Select Column 65 ~ Column 128)
17	/RST	L	Reset signal
18	Vee		Power supply for LCD driving
19	BL+	_	Enable (on/off) for EL B/L
20	BL-	_	No connection

8. Contour Drawing & Block Diagram



9. Function Description

Display On/Off

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

The display data appears when D is and disappears when D is 0. Though the data is not on the screen with D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 into D = 1.

Display Start Line

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	A	A	A	A	A	A

Z address AAAAAA (binary) of the display data RAM is set in the display start line register and displayed at the top of the screen. Figure 2. shows examples of display (1/64 duty cycle) when the start line = 0-3. When the display duty cycle is 1/64 or more (ex. 1/32, 1/24 etc.), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

Set Page (X Address)

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	A	A	A

X address AAA (binary) of the display data RAM is set in the X address register. After that, writing or reading to or from MPU is executed in this specified page until the next page is set. See Figure 1.

Set Y Address

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	A	A	A	A	A	A

Y address AAAAA (binary) of the display data RAM is set in the Y address counter. After that, Y address counter is increased by 1 every time the data is written or read to or from MPU.

Status Read

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	1	Busy	0	On/Off	RESET	0	0	0	0

Busy

When busy is 1, the LSI is executing internal operations. No instruction are accepted while busy is 1, so you should make sure that busy is 0 before writing the next instruction.

•ON/OFF

Shows the liquid crystal display condition: on condition or off condition.

When on/off is 1, the display is in off condition.

When on/off is 0, the display is in on condition.

• RESET

RESET = 1 shows that the system system is being initialized. In this condition, no instructions except status read can be accepted.

RESET = 0 shows that initializing has system is in the usual operation condition.

Write Display Data

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	D	D	D	D	D	D	D	D

Writes 8-bit data DDDDDDDD (binary) into the display data RAM. The Y address is increased by 1 automatically.

Read Display Data

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D	D	D	D	1	D	D	D

Reads out 8-bit data DDDDDDD (binary) from the display data RAM. Then Y address is increased by 1 automatically.

One dummy read is necessary right after the address setting. For details, refer to the explanation of output register in "Function of Each Block".

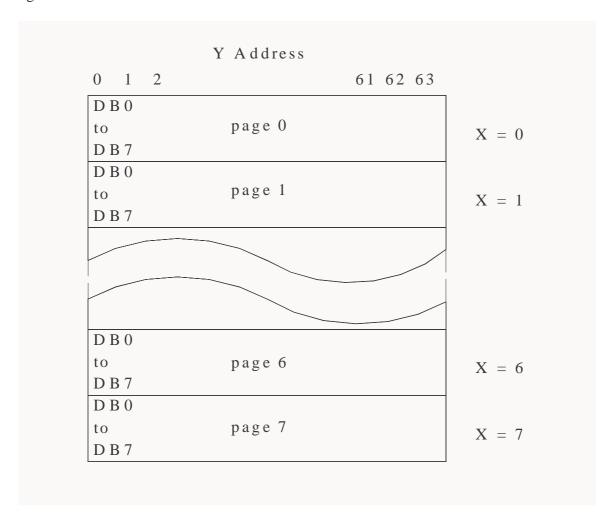


Figure 1.

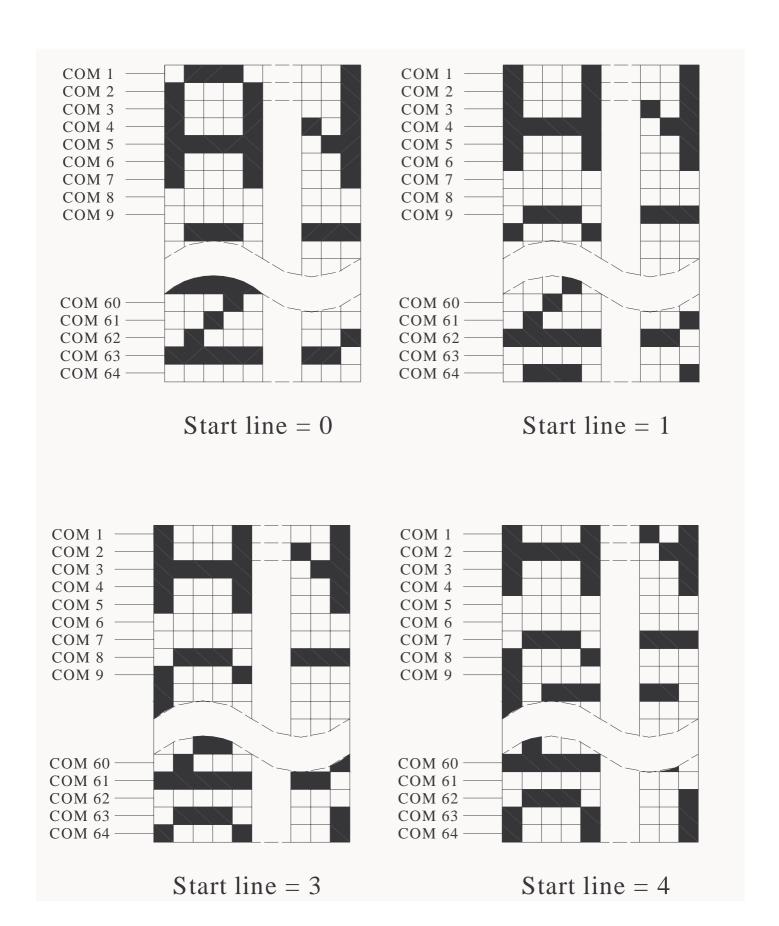


Figure 2.

10.Instruction Table

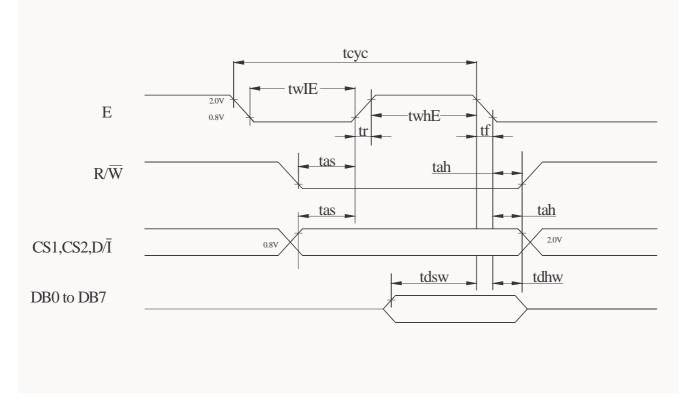
The display control instructions control the internal state of the KS0108B. Instruction is received from MPU to KS0108B for the display control. The following table shows various instructions.

Instruction	D/I	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display ON/OFF	0	0	0	0	1	1	1	1	1	0/1	Controls the display on or off. Internal status and display RAM data are not affected. 0:OFF, 1:ON
Set Address	0	0	0	1		Y	addres	ss (0~	63)		Sets the Y address in the Y address counter.
Set Page (X address)	0	0	1	0	1	1	1	Pa	ge (0 -	~7)	Sets the X address at the X address register.
Display Start Line	0	0	1	1		Displa	ıy star	t line((0~63))	Indicates the display data RAM displayed at the top of the screen.
Status Read	0	1	B U S Y	0	ON/ OFF	R E S E T	0	0	0	0	Read status. BUSY 0:Ready 1:In operation ON/OFF 0:Display ON 1:Display OFF RESET 0:Normal 1:Reset
Write Display Data	1	0			Ι	Displa	y Data	a			Writes data (DB0:7)into display data RAM. After writing instruction, Y address is increased by 1 automatically.
Read Display Data	1	1			I	Displa	y Data	a			Reads data (DB0:7) from display data RAM to the data bus.

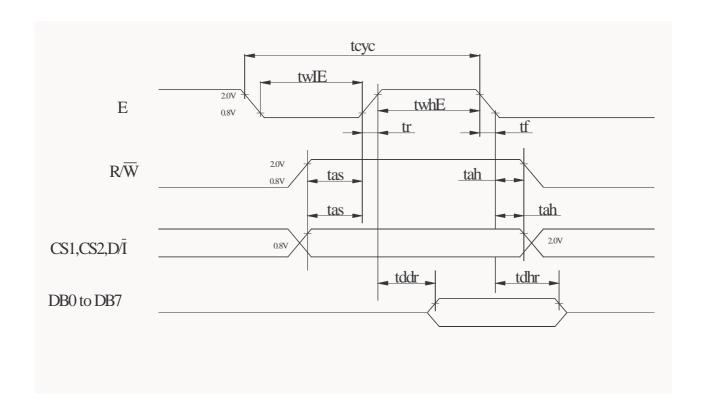
11. Timing Characteristics

MPU Interface

Characteristic	Symbol	Min	Тур	Max	Unit
E cycle	tcyc	1000	_	_	ns
E high level width	twhE	450	_	_	ns
E low level width	twlE	450	_	_	ns
E rise time	tr	_	_	25	ns
E tall time	tf		_	25	ns
Address set-up time	tas	140	_	_	ns
Address hold time	tah	10	_	_	ns
Data set-up time	tdsw	200	_	_	ns
Data delay time	tddr	_	_	320	ns
Data hold time (write)	tdhw	10	_	_	ns
Data hold time (read)	tdhr	20	_	_	ns



MPU Write Timing



MPU Read Timing

12.RELIABILITY

Content of Reliability Test (wide temperature, -20℃~70℃)

	Environmental Test		
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature	Endurance test applying the high storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C 30min 5min 30min 1 cycle	-20°C/70°C 10 cycles	
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude: 15mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5k Ω CS=100pF 1 time	

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: Vibration test will be conducted to the product itself without putting it in a container.

14. Backlight Information

Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Drive Voltage	Vmax	100	110	_	Vrms	25 C
Drive Wave	Fmax	_	400	_	Hz	25 C
Brightness		25	45	_	cd/m ²	110Vrms/400Hz
Chromatism	X	_	0.29	_	_	110Vrms/400Hz
	Y	_	0.38	_	_	110Vrms/400Hz
Life time			5000		hour	110Vrms/400Hz
Color			White		_	

14. <u>Inspection specification</u>

NO	Item			Criterion		AQL	
01	Electrical Testing	 1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect. 					
02	Black or white spots on LCD (display only)	three white of 2.2 Densely space	or black spo ced: No mo	re than two spots o		2.5	
03	LCD black spots, white spots, contaminatio n (non-display	3.1 Round type $\Phi = (x + \frac{x}{4})$ 3.2 Line type: (a)	As following Length	SIZE $ \Phi \le 0.10 $ 0.10 < $ \Phi \le 0.20 $ 0.20 < $ \Phi \le 0.25 $ 0.25 < Φ	Acceptable Q TY Accept no dense 2 1 0 Acceptable Q TY Accept no dense 2 As round type	2.5	
04	Polarizer bubbles	If bubbles are vis judge using bla specifications, to find, must c specify direction	not easy heck in	Size Φ $\Phi \le 0.2$ 0 0.20< $\Phi \le 0.50$ 0.50< $\Phi \le 1.00$ 1.00< Φ Total Q TY	Acceptable Q TY Accept no dense 3 2 0 3	2.5	

NO	Item	Criterion	AQL
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination	
06	Chipped glass	Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length: 6.1 General glass chip: 6.1.1 Chip on panel surface and crack between panels: z: Chip thickness y: Chip width x: Chip length Z≤1/2t Not over viewing x≤1/8a area 1/2t <z≤2t 1="" 2="" 3k="" 6.1.2="" 8a="" are="" chip.="" chips,="" corner="" crack:<="" each="" exceed="" is="" length="" more="" not="" of="" oif="" or="" td="" there="" total="" x="" x≤1=""><td>2.5</td></z≤2t>	2.5
		z: Chip thickness y: Chip width x: Chip length	
		Z≦1/2t Not over viewing x≤1/8a area	

	$1/2t < z \le 2t$	Not exceed 1/3k	x≦1/8a	
	⊙If there are 2 or m	nore chips, x is the	total length of each	
	chip.			

NO	Item	Criterion	AQL
NO 06	Item Glass cra ck	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	AQL
		y: Chip width x: Chip length z: Chip thickness $y \le L$ $x \le 1/8a$ $0 < z \le t$ OIf the chipped area touches the ITO terminal, over $2/3$ of the ITO must remain and be inspected according to electrode terminal specifications. OIf the product will be heat sealed by the customer, the alignment mark not be damaged. 6. 2. 3 Substrate protuberance and internal crack. y: width x : length $y \le 1/3L$ $x \le a$	

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NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	 8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong. 	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB、COB	 10. 1 COB seal may not have pinholes larger than 0. 2mm or contamination. 10. 2 COB seal surface may not have pinholes through to the IC. 10. 3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10. 9 The Scraping testing standard for Copper Coating of PCB 	2.5 2.5 0.65 2.5 2.5 0.65 2.5 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

12.1 No oxidation, contamination, curves or, bends on 2 interface Pin (OLB) of TCP.
12. 2 No cracks on interface pin (OLB) of TCP. 12. 3 No contamination, solder residue or solder balls on product. 12. 4 The IC on the TCP may not be damaged, circuits. 12. 5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever. 12. 6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color. 12. 7 Sealant on top of the ITO circuit has not hardened. 12. 8 Pin type must match type in specification sheet. 12. 9 LCD pin loose or missing pins. 12. 10 Product packaging must the same as specified on



winstar LCM Sample Estimate Feedback Sheet

Modu	le Number :		Page: 1
1, <u>Pa</u>	nnel Specification:		
1.	Panel Type:	☐ Pass	□ NG ,
2.	View Direction:	□ Pass	□ NG ,
3.	Numbers of Dots:	☐ Pass	□ NG ,
4.	View Area:	□ Pass	□ NG ,
5.	Active Area:	□ Pass	□ NG ,
6.	Operating Temperature :	□ Pass	□ NG ,
7.	Storage Temperature:	□ Pass	□ NG ,
8.	Others:		
2、 <u>M</u>	echanical Specification :		
1.	PCB Size:	□ Pass	□ NG ,
2.	Frame Size:	\square Pass	□ NG ,
3.	Materal of Frame:	□ Pass	□ NG ,
4.	Connector Position:	□ Pass	□ NG ,
5.	Fix Hole Position:	□ Pass	□ NG ,
6.	Backlight Position:	□ Pass	□ NG ,
7.	Thickness of PCB:	□ Pass	□ NG ,
8.	Height of Frame to PCB:	□ Pass	□ NG ,
9.	Height of Module:	□ Pass	□ NG ,
10.	Others:	□ Pass	□ NG ,
3, <u>Re</u>	elative Hole Size :		
1.	Pitch of Connector:	□ Pass	□ NG ,
2.	Hole size of Connector:	□ Pass	□ NG ,
3.	Mounting Hole size:	□ Pass	□ NG ,
4.	Mounting Hole Type:	□ Pass	□ NG ,
5.	Others:	□ Pass	□ NG ,
4、 <u>Ba</u>	cklight Specification :		
1.	B/L Type:	☐ Pass	□ NG ,
2.	B/L Color:	☐ Pass	\square NG,
3.	B/L Driving Voltage (Referen	nce for LED	O Type): \square Pass \square NG,
4.	B/L Driving Current:	☐ Pass	\square NG,
5.	Brightness of B/L:	□ Pass	\square NG,
6.	B/L Solder Method:	☐ Pass	\square NG,
7.	Others:	□ Pass	□ NG ,

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Modu	le Number :		Page: 2	
5、 <u>I</u>	Electronic Characteristics of N	<u>Iodule</u> :		
1.	Input Voltage:	☐ Pass	□ NG ,	
2.	Supply Current:	\square Pass	□ NG ,	
3.	Driving Voltage for LCD:	☐ Pass	□ NG ,	
4.	Contrast for LCD:	☐ Pass	□ NG ,	
5.	B/L Driving Method:	□ Pass	□ NG ,	
6.	Negative Voltage Output:	□ Pass	□ NG ,	
7.	Interface Function:	□ Pass	□ NG ,	
8.	LCD Uniformity:	□ Pass	□ NG ,	
9.	ESD test:	□ Pass	□ NG ,	
10.	Others:	□ Pass	□ NG ,	
6, 5	Summary :			
	Sales signature :			
	Customer Signature :		Date : / /	